

## SHEET

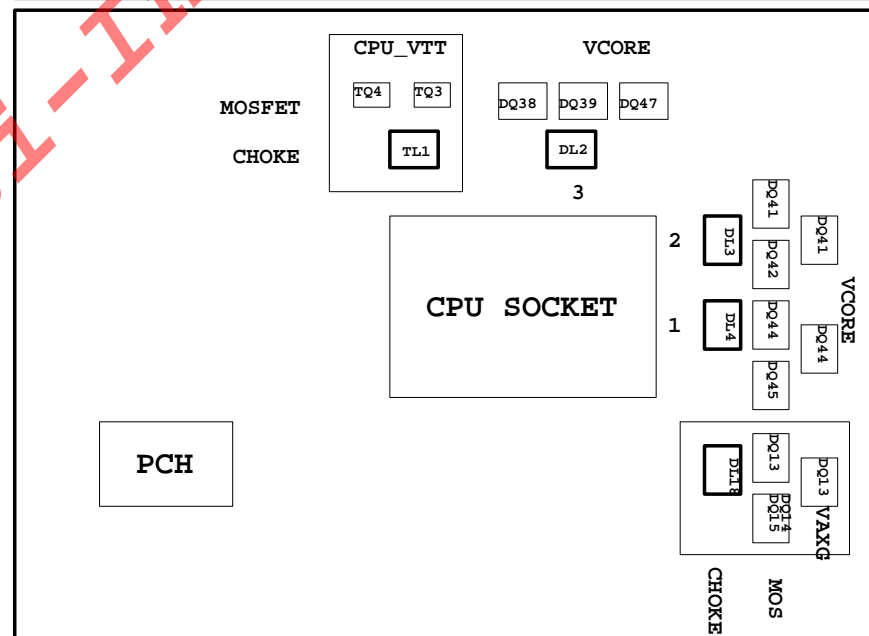
## TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1155-A
05	CPU_LGA1155-B
06	CPU_LGA1155-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESSX4 SLOT / PCIE X1 SLOT
16	PCI SLOT 1~2
17	I/O ITE8728
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19	Dual BIOS
20	ALC887
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24	DISCRETE POWER
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## SHEET

## TITLE

28	F_PANEL , F_USB
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30	HWM,KB/MS , FAN CTRL
31	ARTHEROS AR8161/AR8151
32	mSATA
33	DDR / M3 POWER
34	DVI
35	
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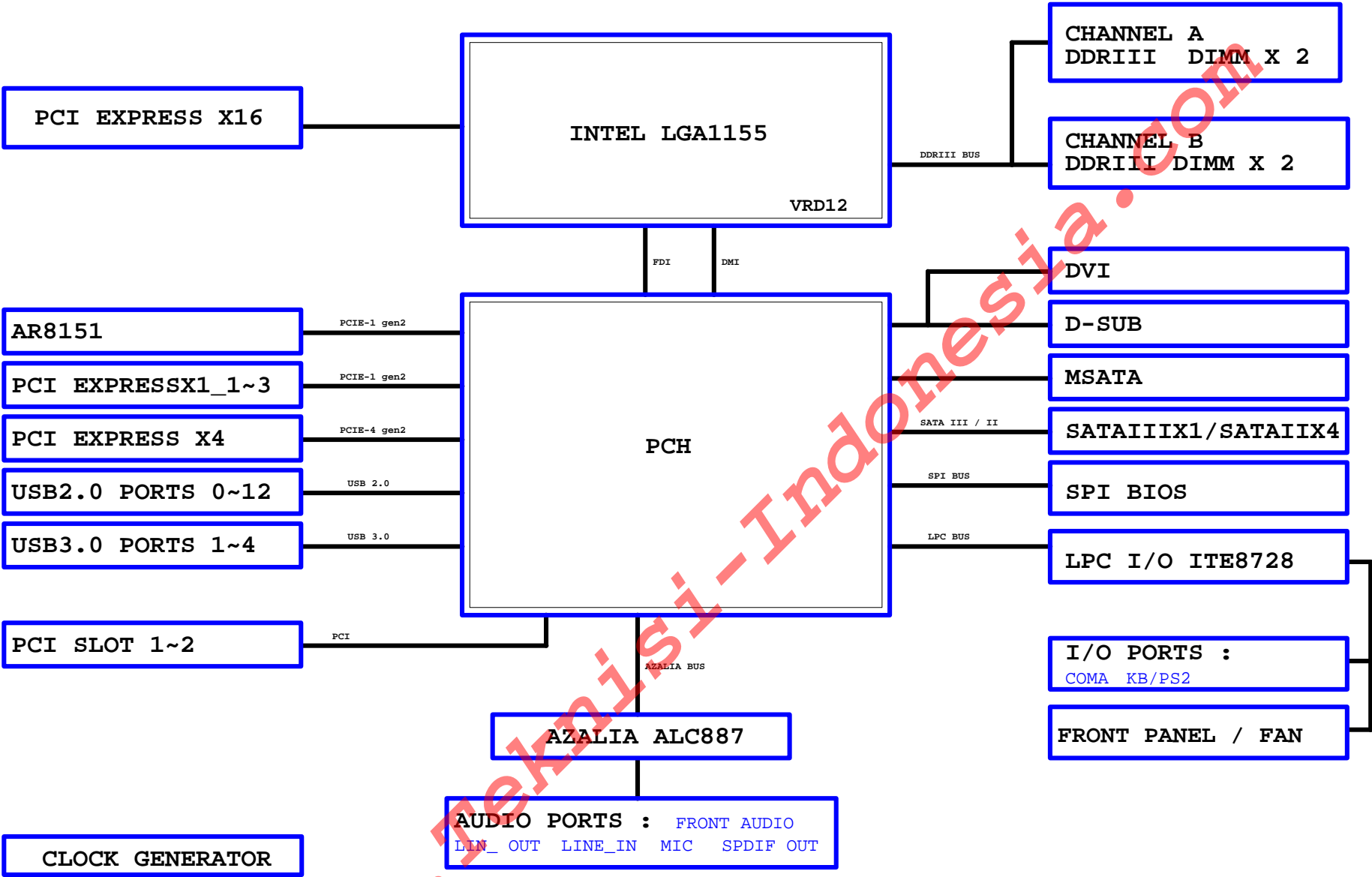
Gigabyte Technology

Title		Cover Sheet	
Size	Document Number	GA-B75-D3V	Rev
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### Component value change history

[illegible][illegible]

BLOCK DIAGRAM





## LGA1155A

MAAA0	AV27	SA_MAI[0]	SA_DQS[0]	AK3	DQSA0
MAAA1	AY24	SA_MAI[1]	SA_DQS[0]	AK2	-DQSA0
MAAA2	AW24	SA_MAI[2]			
MAAA3	AV23	SA_MAI[3]			
MAAA4	AV23	SA_MAI[3]	SA_DQ[0]	AJ3	MDA0
MAAA5	AT24	SA_MAI[4]	SA_DQ[1]	AJ4	MDA1
MAAA6	AT23	SA_MAI[5]	SA_DQ[2]	AL3	MDA2
MAAA7	AU22	SA_MAI[6]	SA_DQ[3]	AL4	MDA3
MAAA8	AV22	SA_MAI[7]	SA_DQ[3]	AJ2	MDA4
MAAA9	AT22	SA_MAI[8]	SA_DQ[4]	AJ1	MDA5
MAAA10	AV28	SA_MAI[9]	SA_DQ[5]	AL2	MDA6
MAAA11	AU21	SA_MAI[10]	SA_DQ[6]	AL1	MDA7
MAAA12	AT21	SA_MAI[11]	SA_DQ[7]		
MAAA13	AW32	SA_MAI[12]		AP3	DQSA1
MAAA14	AU20	SA_MAI[13]	SA_DQS[1]	AP2	-DQSA1
MAAA15	AT20	SA_MAI[14]			
		SA_MAI[15]			
				AN1	MDA8
7	-SWEA	AW29	SA_WE#	AN4	MDA9
7	-SCASA	AV30	SA_CAS#	AN3	MDA10
7	-SRASA	AU28	SA_RAS#	AR3	MDA10
				AN2	MDA11
7	SBA00	AY29	SA_BS[0]	AN2	MDA12
7	SBA01	AW28	SA_BS[1]	AN3	MDA13
7	SBA02	AV20	SA_BS[1]	AR2	MDA14
				AR1	MDA15
7	-CSA0	AU29	SA_CS#	AW4	DQSA2
7	-CSA1	AV32	SA_CS#	AW4	DQSA2
7	-CSA2	AW30	SA_CS#	AW4	DQSA2
7	-CSA3	AU33	SA_CS#		
				AV2	MDA16
7	CKEA0	AV19	SA_CKE[0]	AW3	MDA17
7	CKEA1	AT19	SA_CKE[1]	AW5	MDA18
7	CKEA2	AU18	SA_CKE[2]	AW5	MDA19
7	CKEA3	AV18	SA_CKE[3]	AU2	MDA20
				AU3	MDA21
				AU5	MDA22
				AY5	MDA23
				AV8	DQSA3
				AW8	-DQSA3
7	DCLKA0	AY25	SA_CK[0]	AY7	MDA24
7	-DCLKA0	AW25	SA_CK[0]	AU7	MDA25
7	DCLKA1	AU24	SA_CK[1]	AV9	MDA26
7	-DCLKA1	AU25	SA_CK[1]	AU9	MDA27
7	DCLKA2	AW27	SA_CK[2]	AV7	MDA28
7	-DCLKA2	AY27	SA_CK[2]	AW7	MDA29
7	DCLKA3	AV26	SA_CK[3]	AW9	MDA30
7	-DCLKA3	AW26	SA_CK[3]	AY9	MDA31
				AV37	DQSA4
				AV36	-DQSA4
				AU35	MDA32
				AW37	MDA33
				AU39	MDA34
				AU36	MDA35
				AW35	MDA36
				AY36	MDA37
				AU38	MDA38
				AU37	MDA39
				AP38	DQSA5
				AP39	-DQSA5
				AR40	MDA40
				AR37	MDA41
				AN38	MDA42
				AN37	MDA43
				AR39	MDA44
				AR38	MDA45
				AN39	MDA46
				AN40	MDA47
				AK38	DQSA6
				AK39	-DQSA6
				AL40	MDA48
				AL37	MDA49
				AJ38	MDA50
				AJ37	MDA51
				AL39	MDA52
				AL38	MDA53
				AJ39	MDA54
				AJ40	MDA55
				AF38	DQSA7
				AF39	-DQSA7
				AG40	MDA56
				AG37	MDA57
				AE38	MDA58
				AE37	MDA59
				AG39	MDA60
				AG38	MDA61
				AE39	MDA62
				AE40	MDA63

DDR\_0

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CPU-SK/1155/S/15

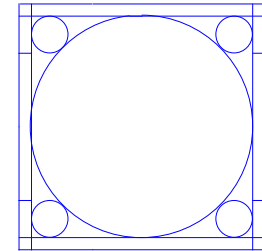
## LGA1155B

MAAB0	AK24	SB_MAI[0]	SB_DQS[0]	AH7	DQSB0
MAAB1	AM20	SB_MAI[1]	SB_DQS[0]	AH6	-DQSB0
MAAB2	AM19	SB_MAI[2]			
MAAB3	AK18	SB_MAI[3]			
MAAB4	AP19	SB_MAI[4]	SB_DQ[0]	AG7	MDB0
MAAB5	AP18	SB_MAI[5]	SB_DQ[1]	AG8	MDB1
MAAB6	AM18	SB_MAI[6]	SB_DQ[2]	AJ9	MDB2
MAAB7	AL18	SB_MAI[7]	SB_DQ[3]	AJ8	MDB3
MAAB8	AL18	SB_MAI[8]	SB_DQ[4]	AG5	MDB4
MAAB9	AY17	SB_MAI[9]	SB_DQ[5]	AG6	MDB5
MAAB10	AN23	SB_MAI[10]	SB_DQ[6]	AJ6	MDB6
MAAB11	AU17	SB_MAI[11]	SB_DQ[7]	AJ7	MDB7
MAAB12	AT18	SB_MAI[12]			
MAAB13	AR26	SB_MAI[13]	SB_DQS[1]	AM8	DQSB1
MAAB14	AY16	SB_MAI[14]	SB_DQS[1]	AL8	-DQSB1
MAAB15	AV16	SB_MAI[15]			
				AM7	MDB8
				AM7	MDB9
				AM10	MDB10
				AL10	MDB11
				AL6	MDB12
				AL9	MDB13
				AL9	MDB14
				AM9	MDB15
				AR8	DQSB2
				AP8	-DQSB2
				AP7	MDB16
				AR7	MDB17
				AP10	MDB18
				AR10	MDB19
				AP6	MDB20
				AR6	MDB21
				AP9	MDB22
				AR9	MDB23
				AN13	DQSB3
				AN12	-DQSB3
				AM12	MDB24
				AM12	MDB25
				AR13	MDB26
				AP13	MDB27
				AL12	MDB28
				AL13	MDB29
				AR12	MDB30
				AP12	MDB31
				AN29	DQSB4
				AN28	-DQSB4
				AR28	MDB32
				AP28	MDB33
				AL28	MDB34
				AL29	MDB35
				AP28	MDB36
				AP28	MDB37
				AN28	MDB38
				AN29	MDB39
				AP33	DQSB5
				AR33	-DQSB5
				AP32	MDB40
				AP21	MDB41
				AP35	MDB42
				AP34	MDB43
				AR32	MDB44
				AR31	MDB45
				AR35	MDB46
				AR34	MDB47
				AL33	DQSB6
				AM33	-DQSB6
				AM32	MDB48
				AM31	MDB49
				AL35	MDB50
				AL32	MDB51
				AM34	MDB52
				AL31	MDB53
				AM35	MDB54
				AL34	MDB55
				AG35	DQSB7
				AG34	-DQSB7
				AH35	MDB56
				AH34	MDB57
				AE34	MDB58
				AE35	MDB59
				AJ35	MDB60
				AJ34	MDB61
				AF33	MDB62
				AF35	MDB63

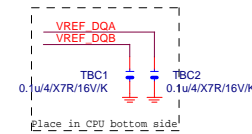
DDR\_1

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CPU-SK/1155/S/15

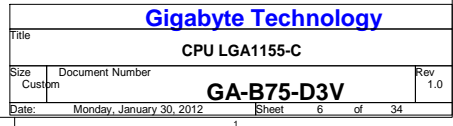
LGA1155  
ILM\_BP/1156/CSP

Need check the new CPU ME

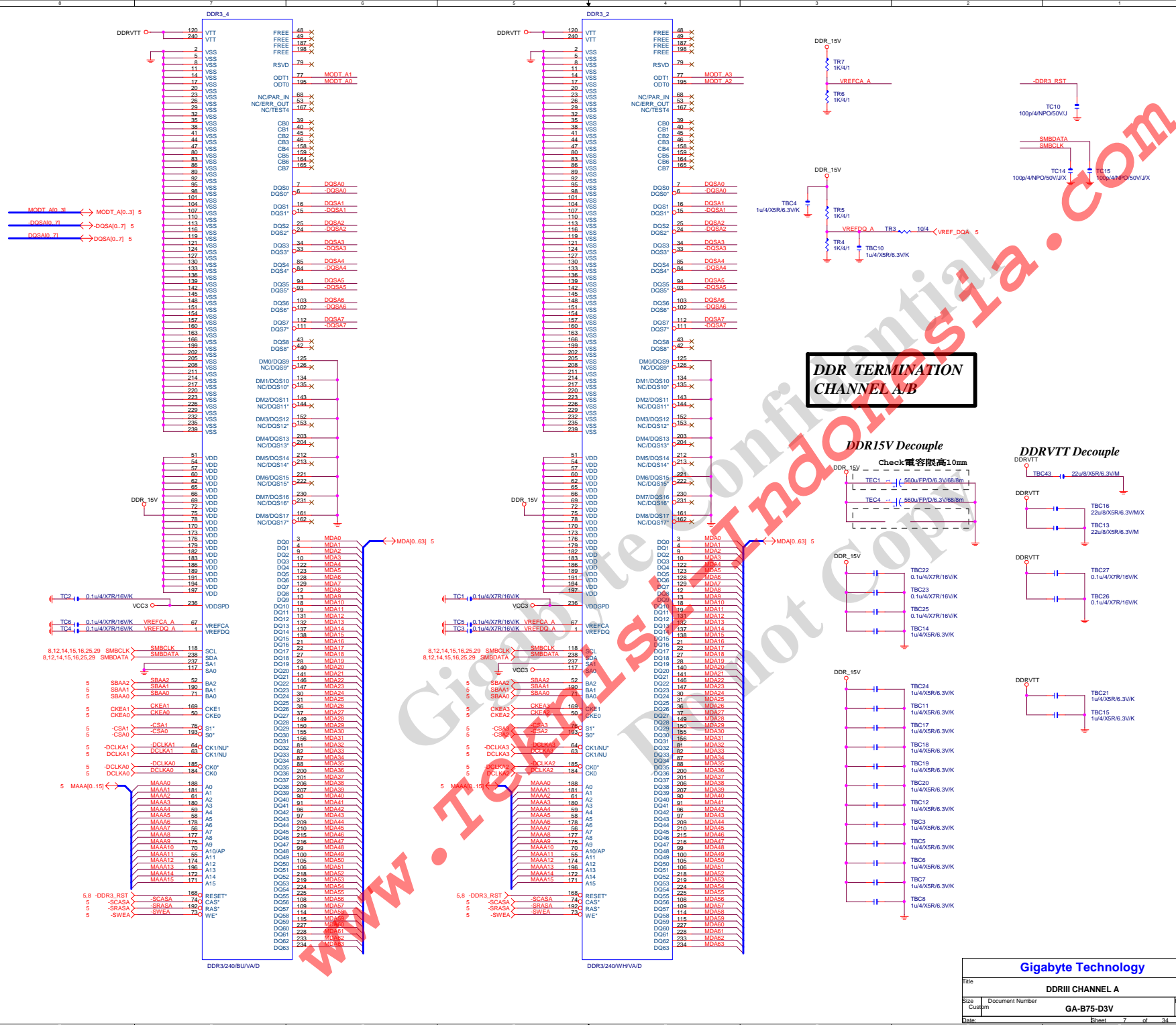


Gigabyte Technology

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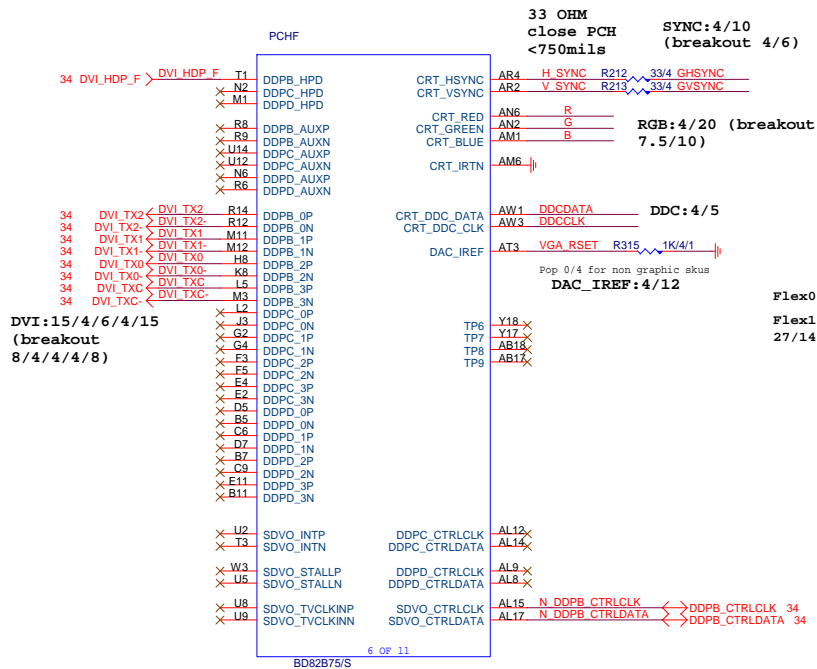






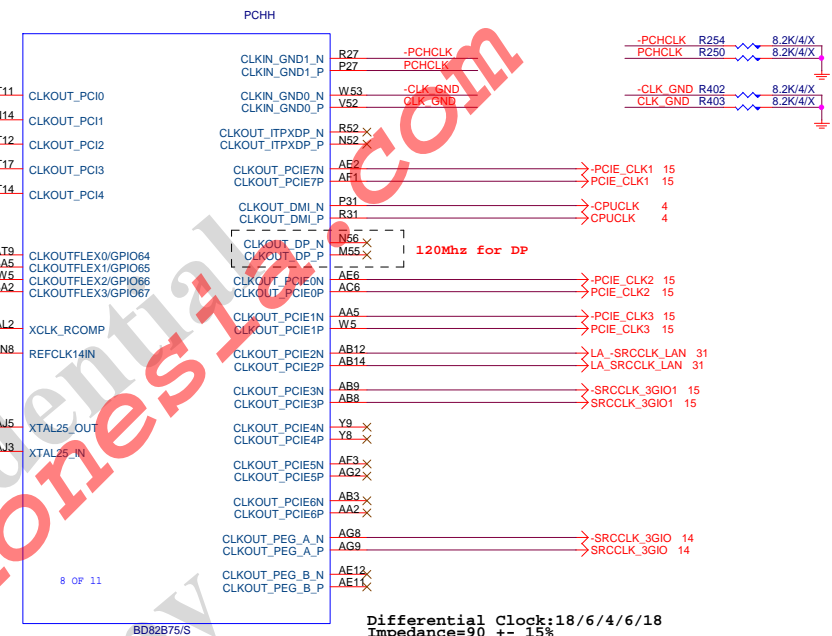
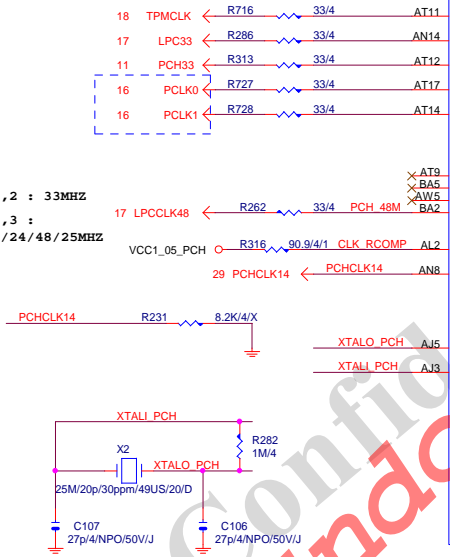




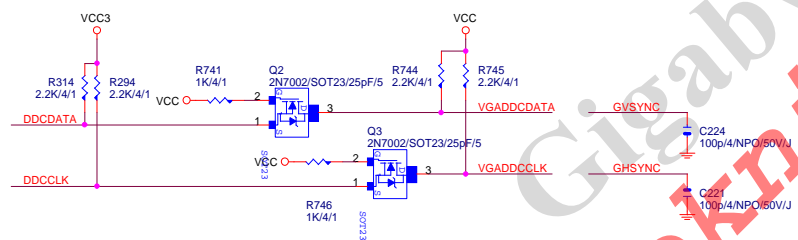


RGB:4/20  
(breakout  
7.5/10)

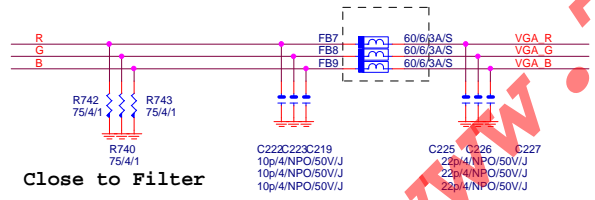
Flex0,2 : 33MHZ  
Flex1,3 :  
27/14/24/48/25MHZ



# VGA DDC

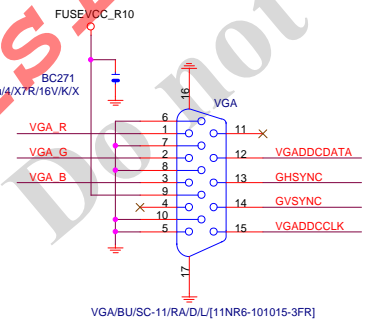


# VGA DDC

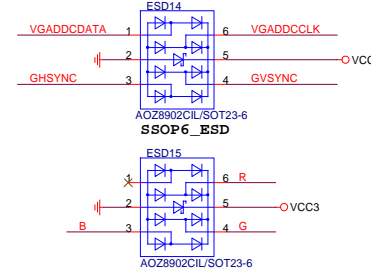


Close to Filter

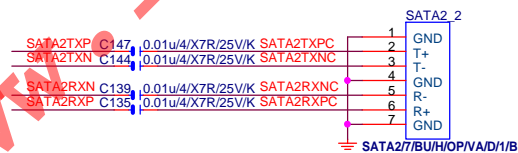
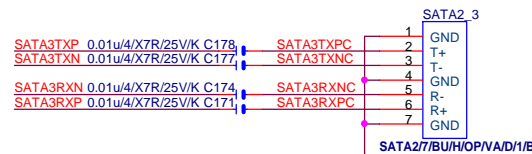
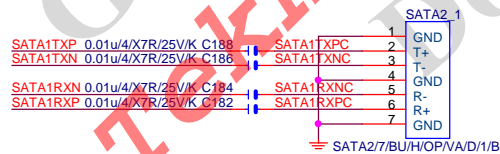
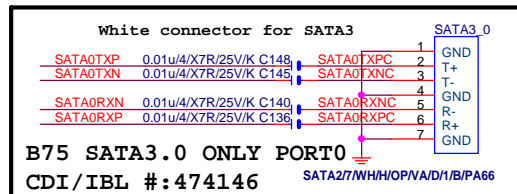
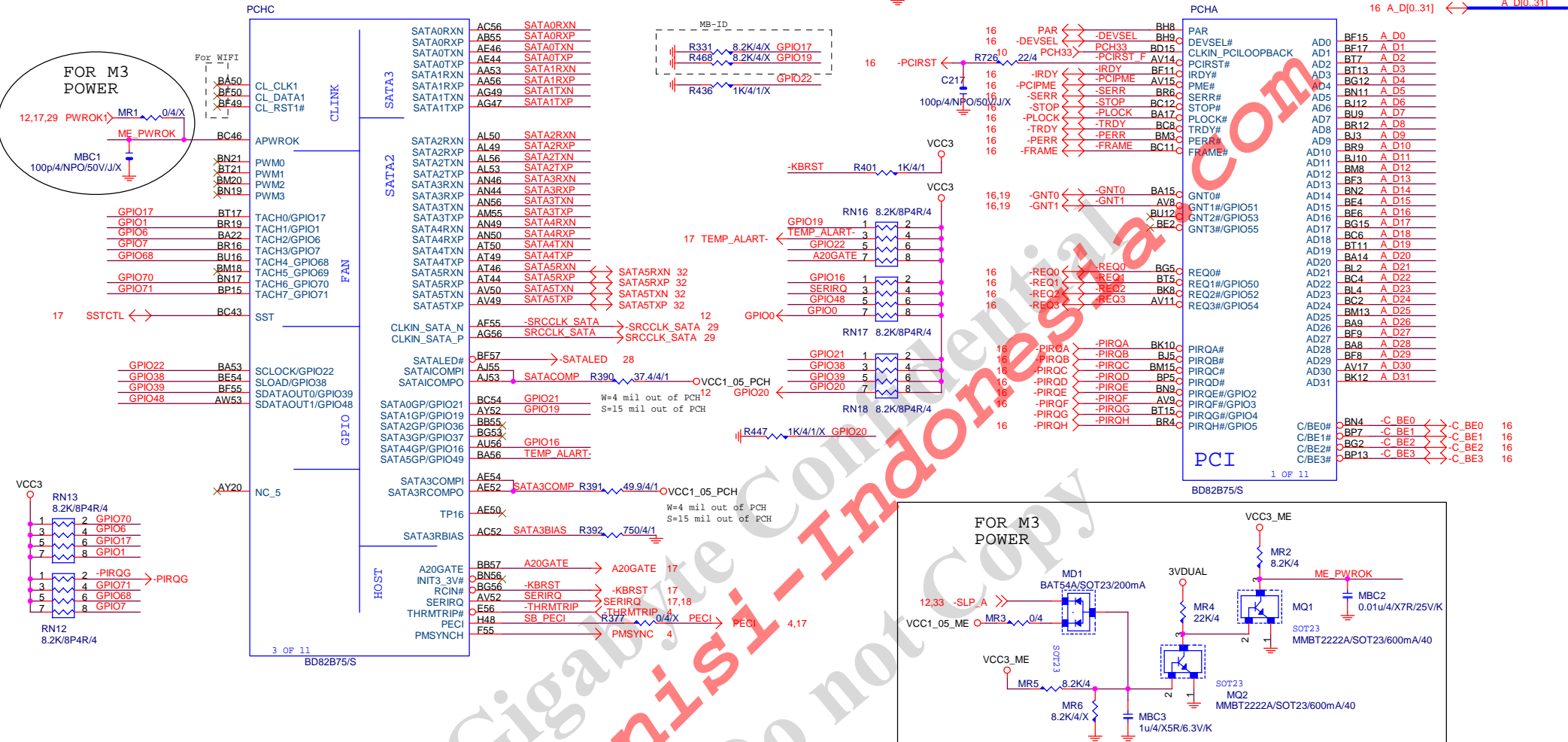
# VGA CONNECTOR



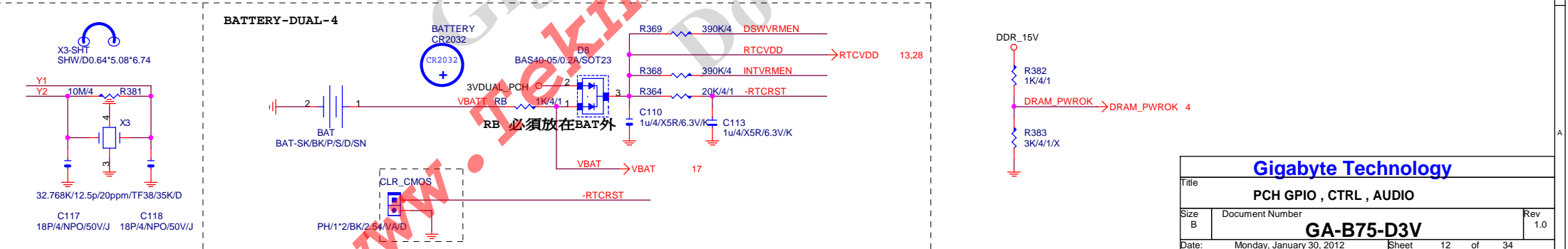
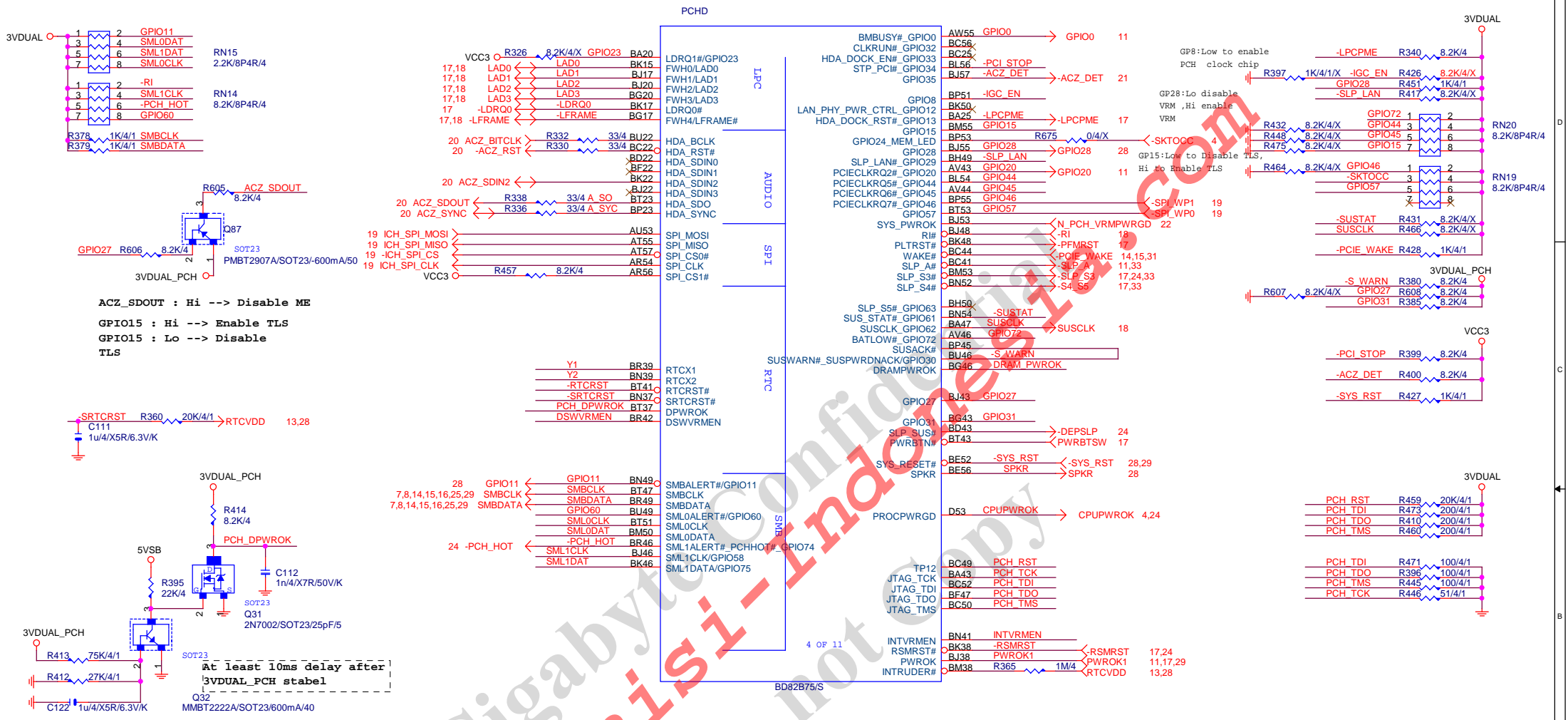
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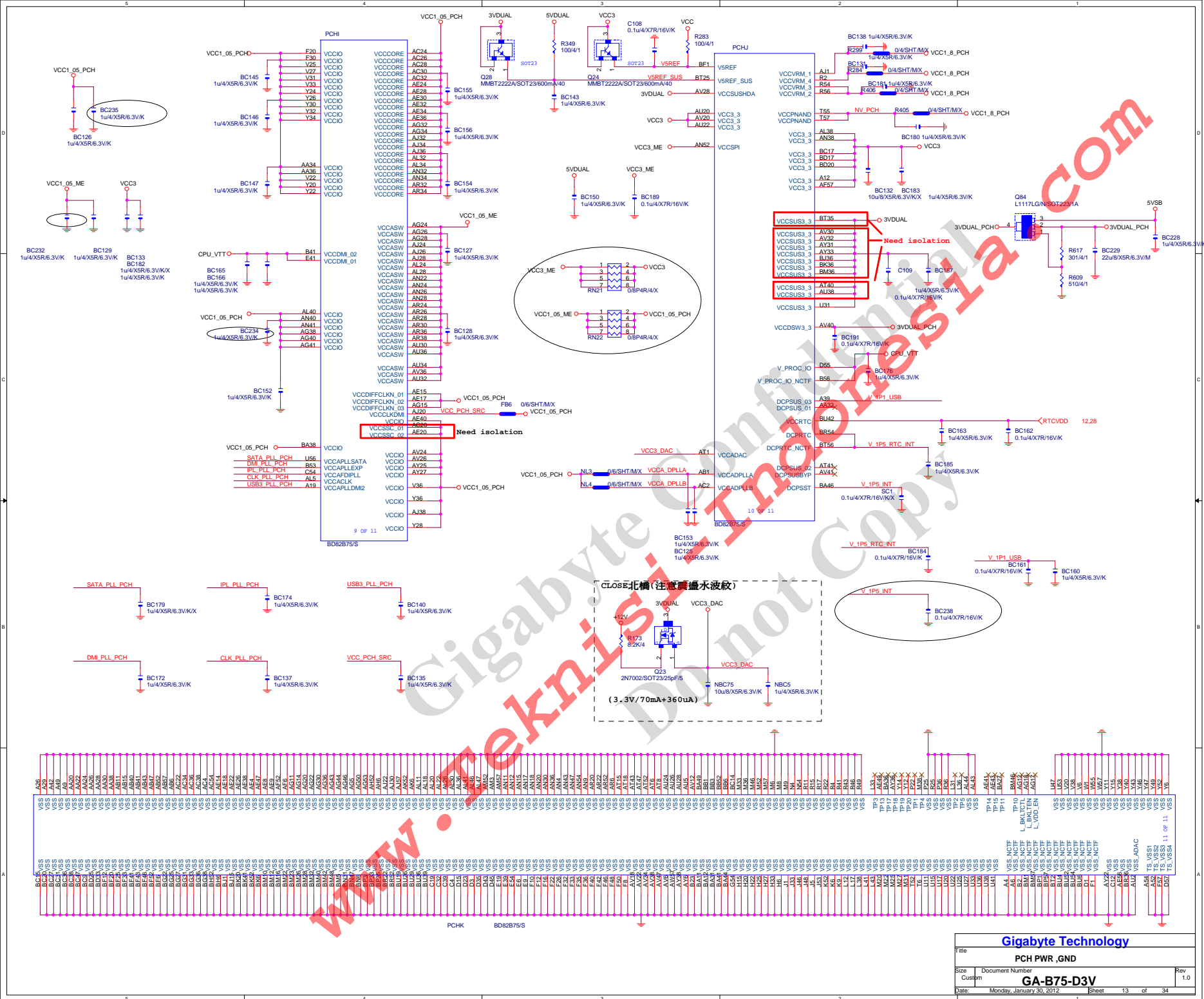


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Title			
PCH DISPLAY ,CLK BUFFER			
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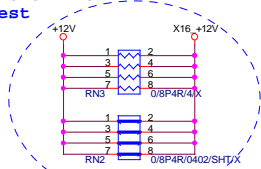
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Title <b>PCH HOST , SATA, PCI</b>			
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+12 protect short-wire test



PCIE16:16/5/5/5/16

EXP\_RXP0\_15] >>> EXP\_RXP[0..15] 4  
EXP\_RXN0\_15] >>> EXP\_RXN[0..15] 4  
EXP\_TXP0\_15] >>> EXP\_TXP[0..15] 4  
EXP\_TXN0\_15] >>> EXP\_TXN[0..15] 4

EXP_TXP0	C43	0.22u/4X5R6.3V/KEXP_TXP0C
EXP_TXN0	C36	0.22u/4X5R6.3V/KEXP_TXN0C
EXP_TXP1	C47	0.22u/4X5R6.3V/KEXP_TXP1C
EXP_TXN1	C49	0.22u/4X5R6.3V/KEXP_TXN1C
EXP_TXP2	C52	0.22u/4X5R6.3V/KEXP_TXP2C
EXP_TXN2	C54	0.22u/4X5R6.3V/KEXP_TXN2C
EXP_TXP3	C57	0.22u/4X5R6.3V/KEXP_TXP3C
EXP_TXN3	C59	0.22u/4X5R6.3V/KEXP_TXN3C
EXP_TXP4	C62	0.22u/4X5R6.3V/KEXP_TXP4C
EXP_TXN4	C64	0.22u/4X5R6.3V/KEXP_TXN4C
EXP_TXP5	C65	0.22u/4X5R6.3V/KEXP_TXP5C
EXP_TXN5	C67	0.22u/4X5R6.3V/KEXP_TXN5C
EXP_TXP6	C69	0.22u/4X5R6.3V/KEXP_TXP6C
EXP_TXN6	C71	0.22u/4X5R6.3V/KEXP_TXN6C
EXP_TXP7	C76	0.22u/4X5R6.3V/KEXP_TXP7C
EXP_TXN7	C75	0.22u/4X5R6.3V/KEXP_TXN7C
EXP_TXP8	C79	0.22u/4X5R6.3V/KEXP_TXP8C
EXP_TXN8	C80	0.22u/4X5R6.3V/KEXP_TXN8C
EXP_TXP9	C81	0.22u/4X5R6.3V/KEXP_TXP9C
EXP_TXN9	C82	0.22u/4X5R6.3V/KEXP_TXN9C
EXP_TXP10	C86	0.22u/4X5R6.3V/KEXP_TXP10C
EXP_TXN10	C87	0.22u/4X5R6.3V/KEXP_TXN10C
EXP_TXP11	C90	0.22u/4X5R6.3V/KEXP_TXP11C
EXP_TXN11	C91	0.22u/4X5R6.3V/KEXP_TXN11C
EXP_TXP12	C92	0.22u/4X5R6.3V/KEXP_TXP12C
EXP_TXN12	C93	0.22u/4X5R6.3V/KEXP_TXN12C
EXP_TXP13	C95	0.22u/4X5R6.3V/KEXP_TXP13C
EXP_TXN13	C96	0.22u/4X5R6.3V/KEXP_TXN13C
EXP_TXP14	C97	0.22u/4X5R6.3V/KEXP_TXP14C
EXP_TXN14	C98	0.22u/4X5R6.3V/KEXP_TXN14C
EXP_TXP15	C99	0.22u/4X5R6.3V/KEXP_TXP15C
EXP_TXN15	C100	0.22u/4X5R6.3V/KEXP_TXN15C

PCI-E REV:1.1--> 2.5GHZ

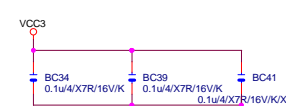
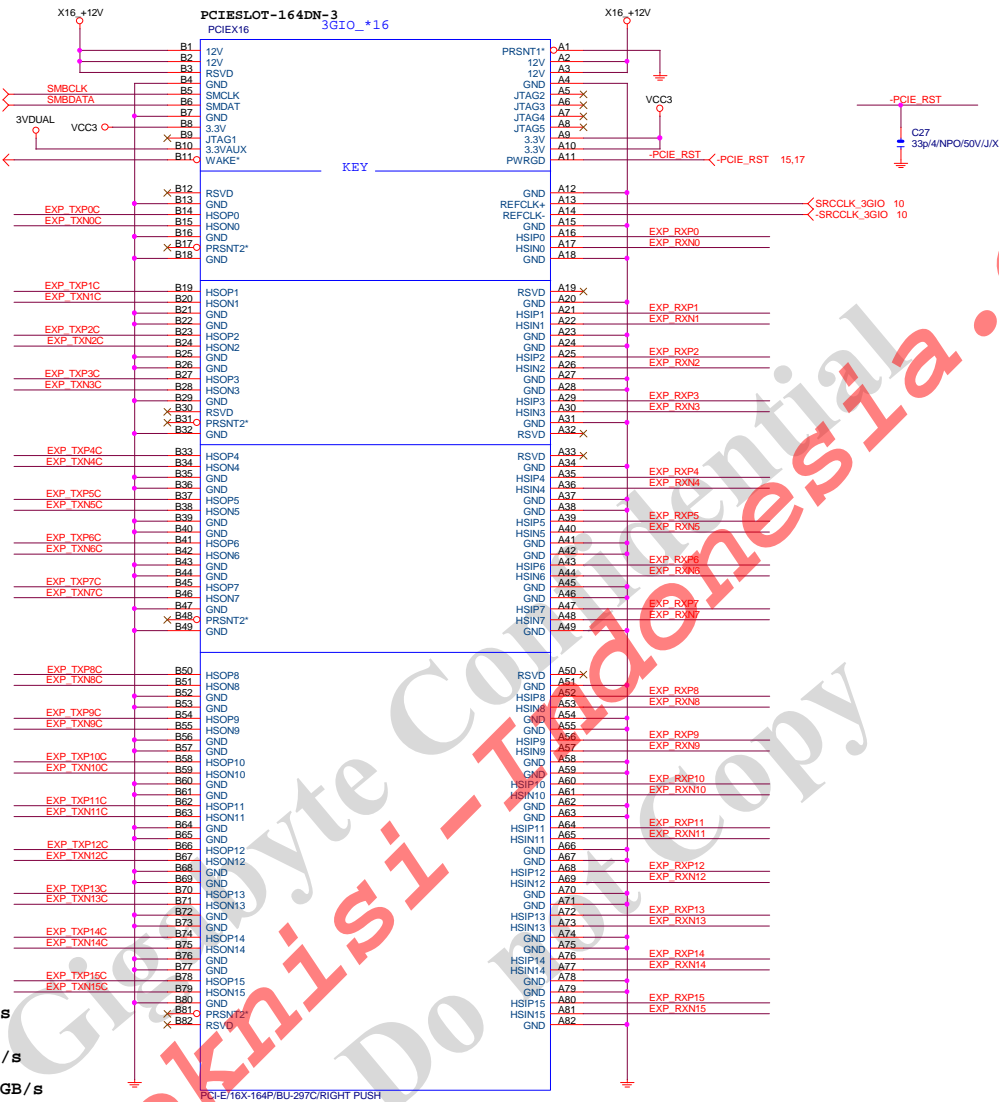
PCE-E X1(單向) BANDWITH=2.5GHZ\*(8b/10b)=2Gb/s=250MB/s

PCE-E X1(雙向) BANDWITH=2.5GHZ\*(8b/10b)X2=4Gb/s=500MB/s

PCE-E X16(單向) BANDWITH=2.5GHZ\*(8b/10b)X16=32Gb/s=4GB/s

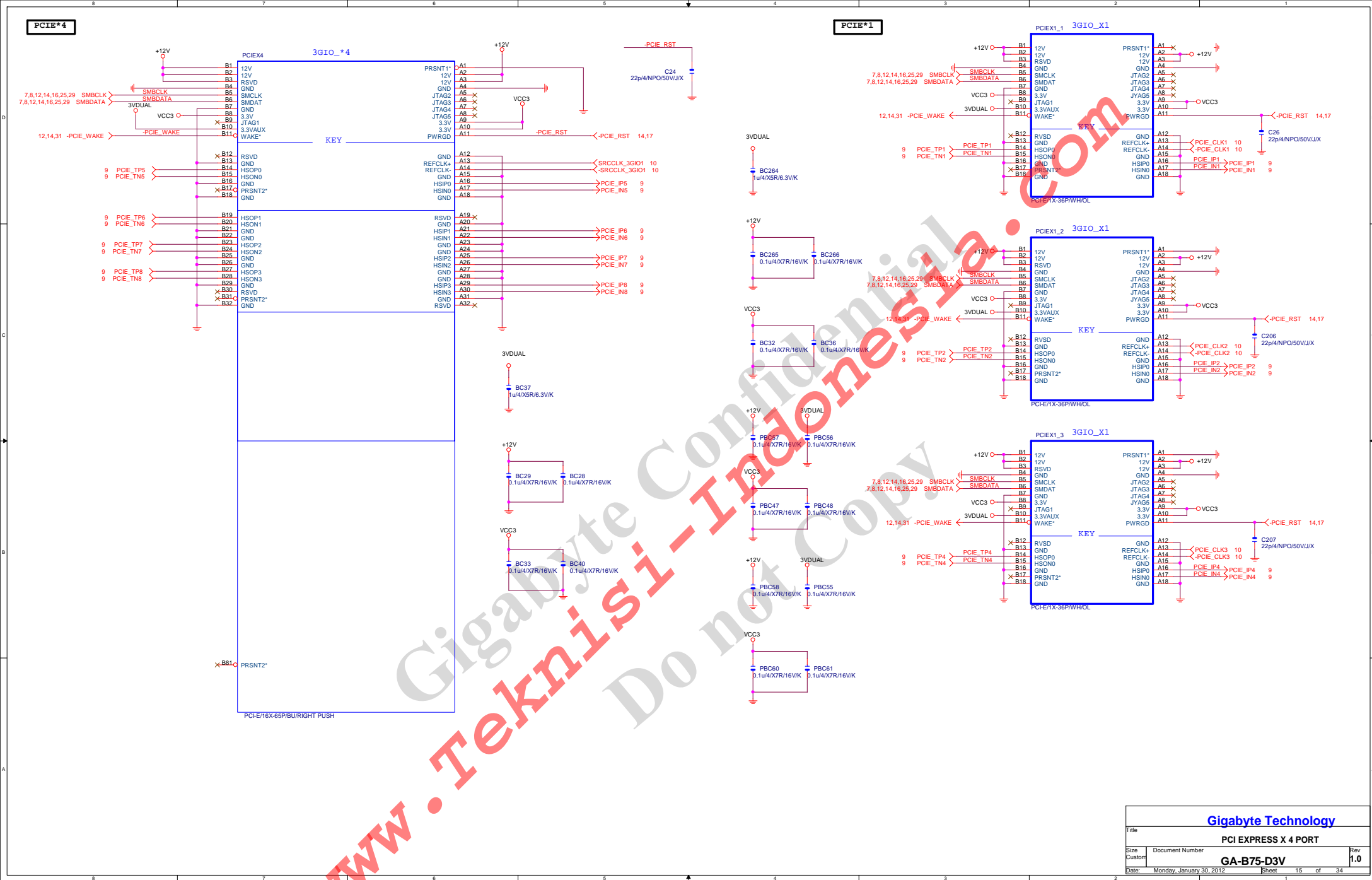
PCE-E X16(雙向) BANDWITH=2.5GHZ\*(8b/10b)X16X2=64Gb/s=8GB/s

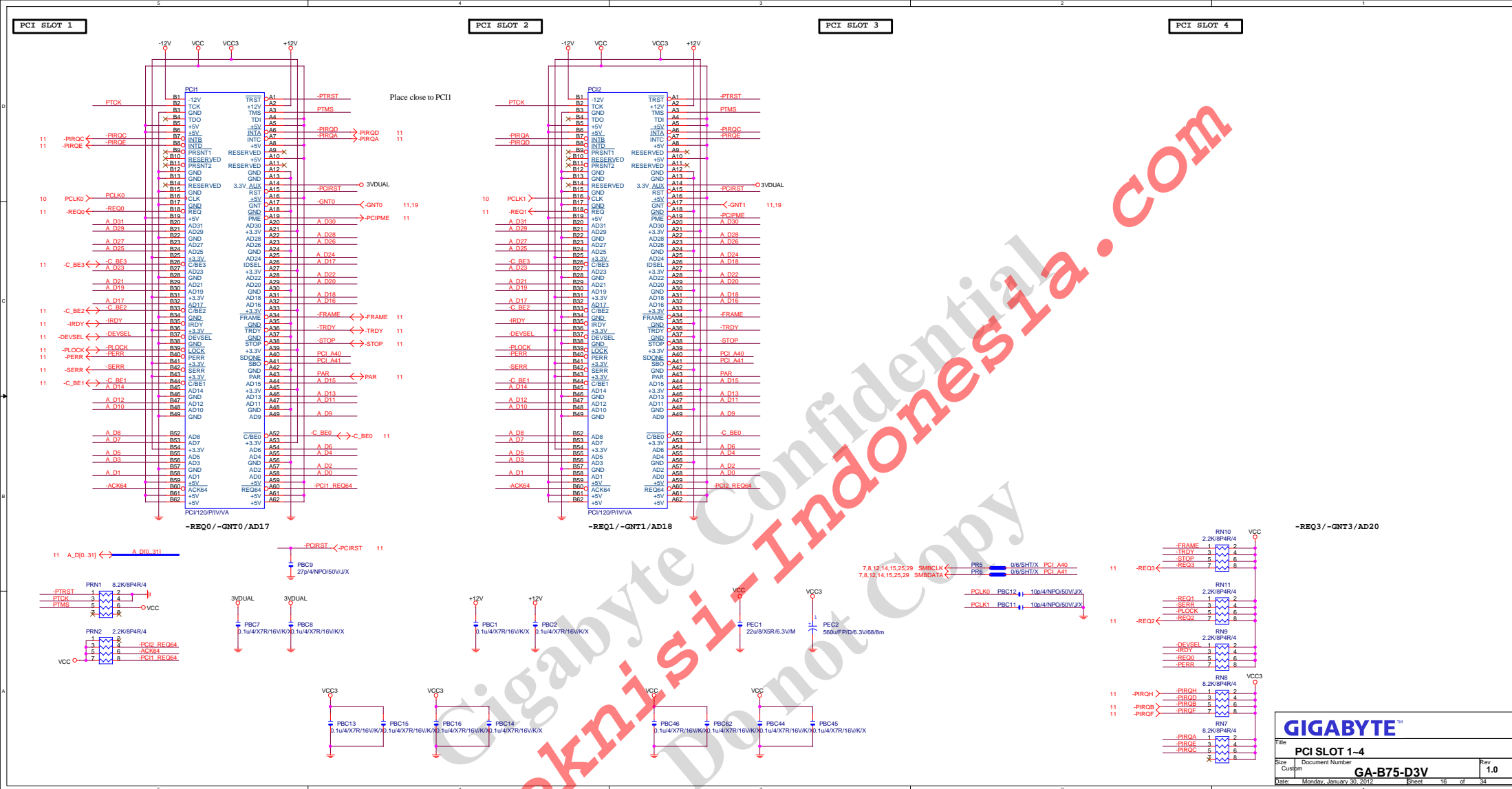
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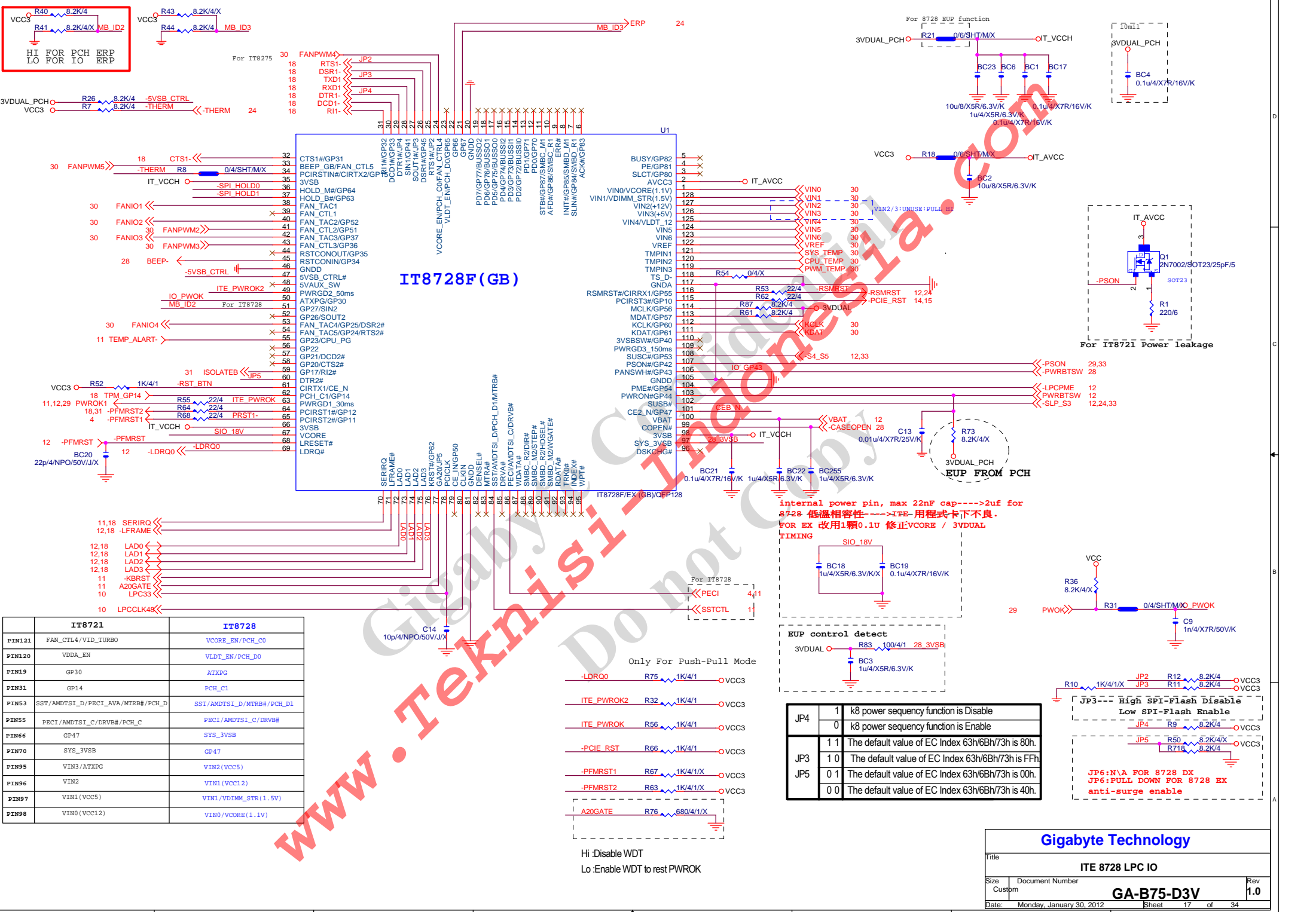


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	IT8721	IT8728
PIN121	FAN_CTL4/VID_TURBO	VCORE_EN/PCH_C0
PIN120	VDDA_EN	VLDT_EN/PCH_D0
PIN19	GP30	ATXPG
PIN31	GP14	PCH_C1
PIN53	SST/AMDTSI_D/PECI_AVA/MTRB#/PCH_D	SST/AMDTSI_D/MTRB#/PCH_D1
PIN55	PECI/AMDTSI_C/DRVB#/PCH_C	PECI/AMDTSI_C/DRVB#
PIN66	GP47	SYS_3VSB
PIN70	SYS_3VSB	GP47
PIN95	VIN3/ATXPG	VIN2 (VCC5)
PIN96	VIN2	VIN1 (VCC12)
PIN97	VIN1 (VCC5)	VIN1/VDIMM_STR (1.5V)
PIN98	VIN0 (VCC12)	VIN0/VCORE (1.1V)

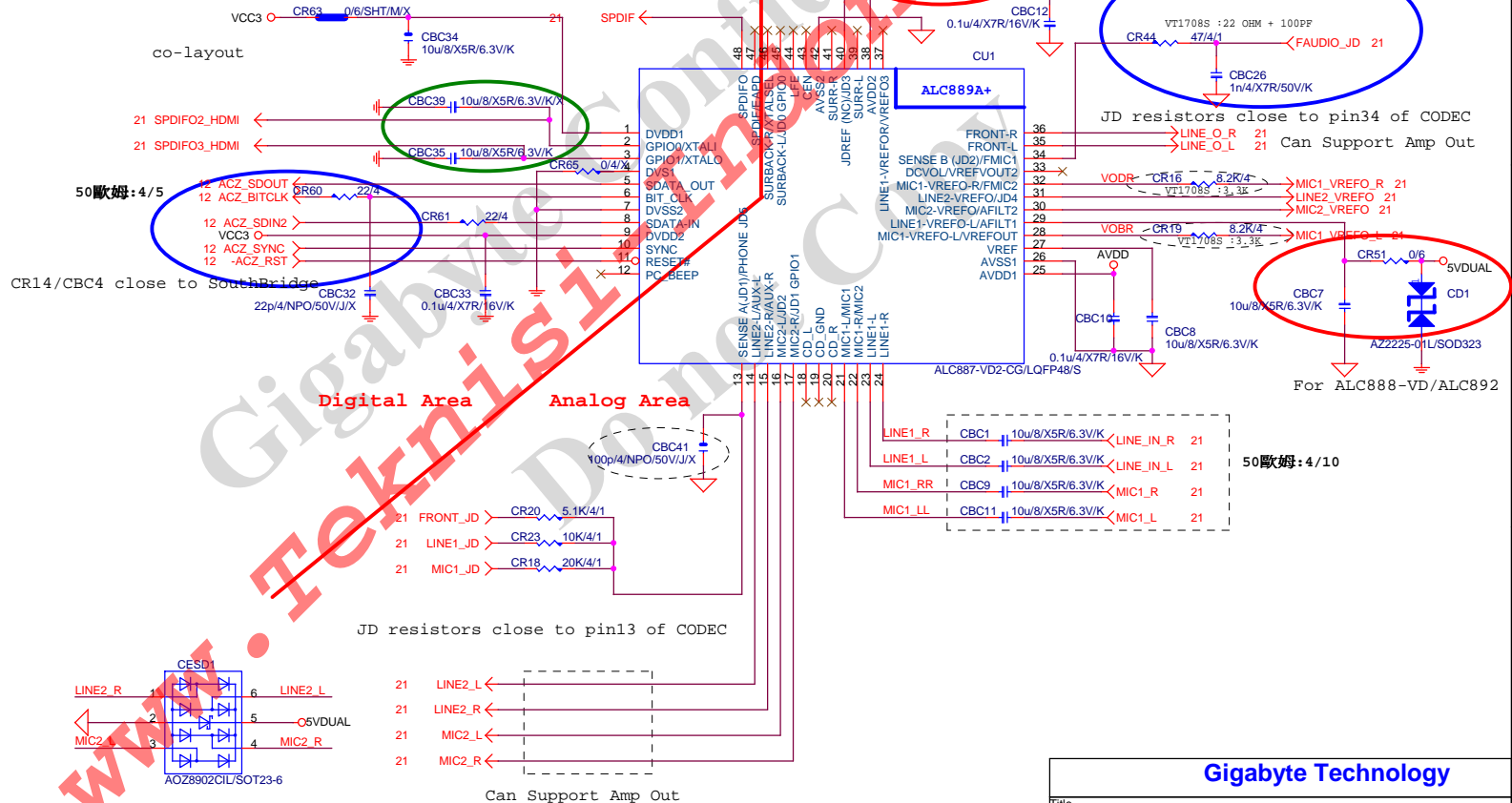
JP4	1	k8 power sequency function is Disable
JP4	0	k8 power sequency function is Enable
JP3	1 0	The default value of EC Index 63h/6Bh/73h is FFh
JP5	0 1	The default value of EC Index 63h/6Bh/73h is 00h
JP5	0 0	The default value of EC Index 63h/6Bh/73h is 40h





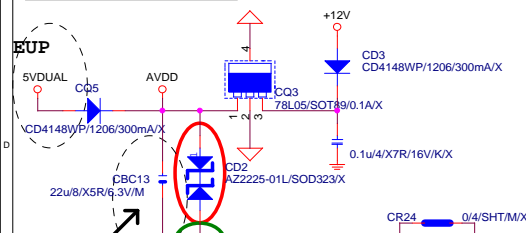
Gigabyte Technology			
Title		BIOS	
Size	Document Number	GA-B75-D3V	Rev
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	ALC662	ALC887-VD2	ALC889	VT1708S-CD	VT1708S-CE	VT2021
CR49	X	X	O	O	X	O
CBC36	O	O	X	X	O	X
CR28/CBC11	47ohm+1nF	47ohm+1nF	47ohm+1nF	22ohm+100P	22ohm+100P	47ohm+1nF
CR52	X	O	O	O	O	O
CR57	O	X	X	X	X	X
CBC1/CBC2	10uF/X5R	10uF/X5R	22uF/X5R	10uF/X5R	10uF/X5R	10uF/X5R
CR18	5.11K/4/1	5.11K/4/1	5.11K/4/1	5.1K/4/1	5.1K/4/1	5.1K/4/1
CR36	20K/4/1	20K/4/1	20K/4/1	5.1K/4/1	20K/4/1	5.1K/4/1
CBC38/CBC39	X	X	X	100P/4	100P/4	X
CR10/CR8/CR20/CR45/ CR42/CR51/CR43/CR22/ CR27/CR26	22K/4	22K/4	22K/4	10K/4/1	10K/4/1	10K/4/1
CR7/CR9/CR5/CR13/ CR29/CR32/CR46/CR19/ CR50/CR41/CR21/CR47/ CR2/CR11/CR14/CR24	62 ohm	62 ohm	62 ohm	75 ohm	75 ohm	75 ohm
CFB1/CD1/CBC4	O	O	X	X	O	O
CD2/CD3/CQ3/CQ4	X	X	O	O	X	X





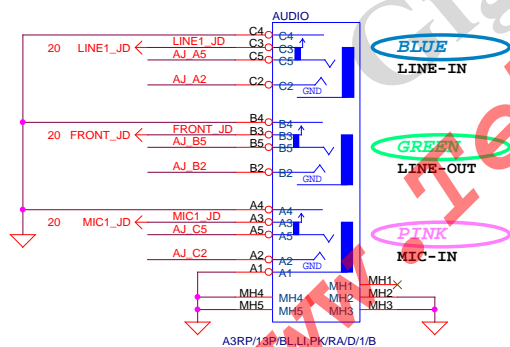
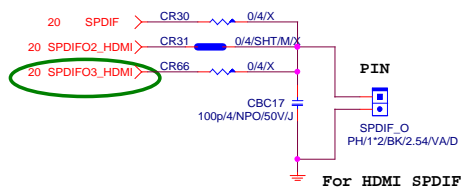
## CODEC POWER/EMI PAD



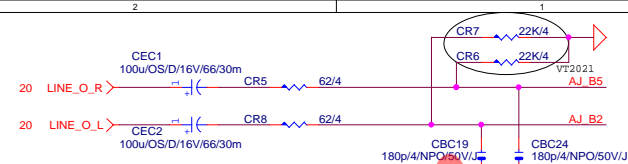
上ALC892時,此顆電容要保留

ADD CD2 For ESD PROTECT DIODE

## SPDIF\_OUT

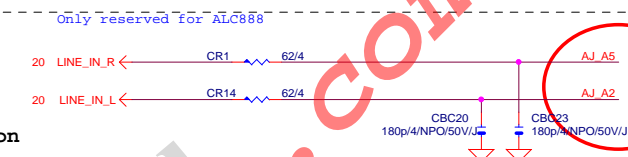
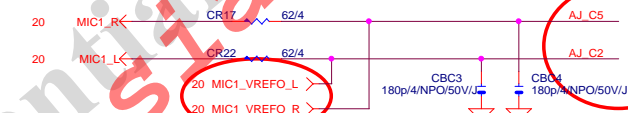


## LINE-OUT

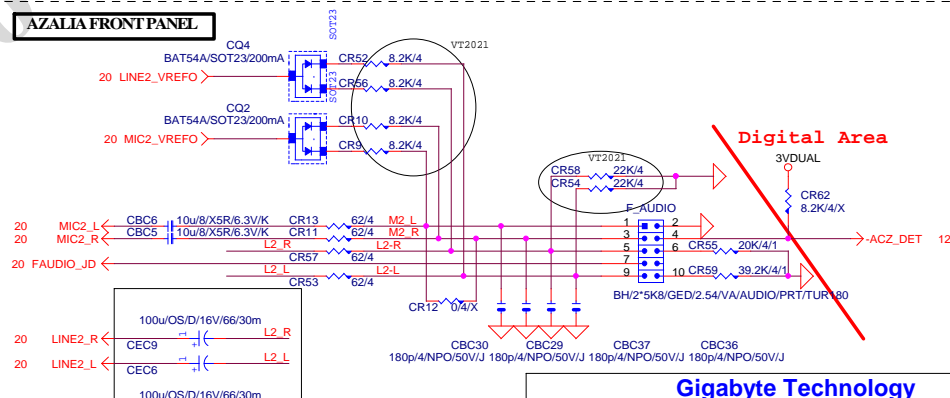


## LINE-IN

Verify MIC function  
in LINE-in

**MIC-IN**

## AZALIA FRONT PANEL



## Digital Area

## Gigabyte Technology

Title			
AUDIO JACK			
Size Custom	Document Number	GA-B75-D3V	Rev 1.0
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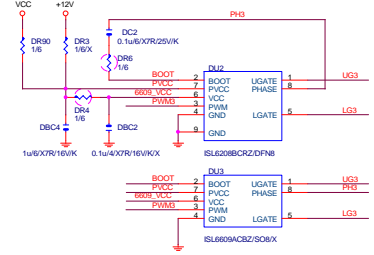




[1]



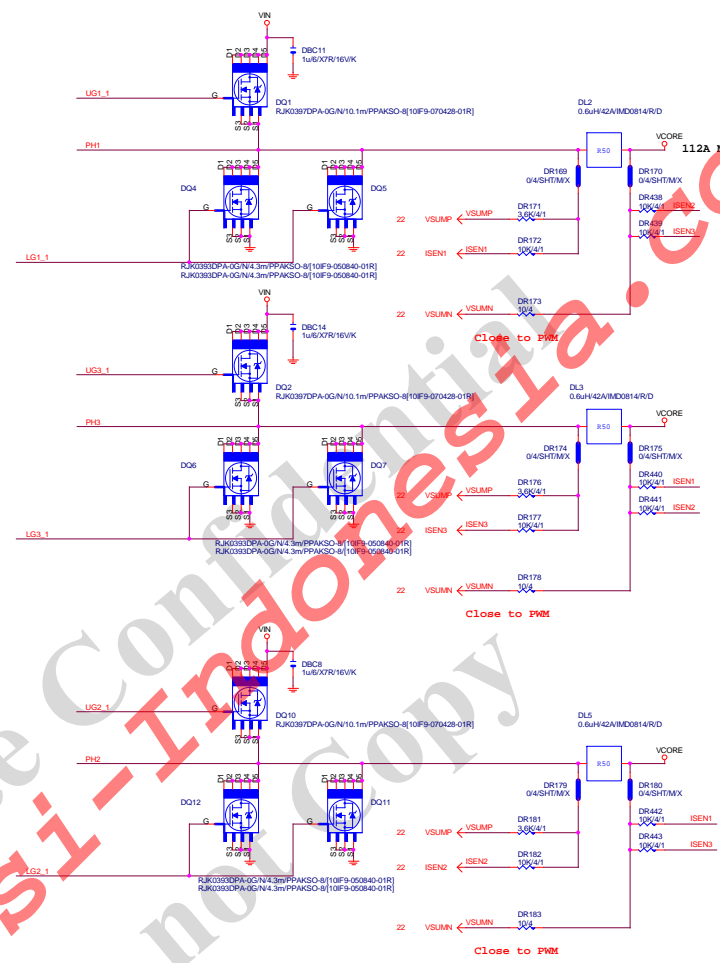
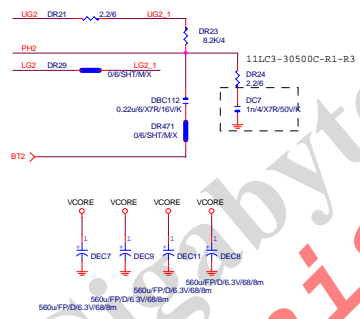
Pop 15L66250N for P01  
 (15L66250N/008)



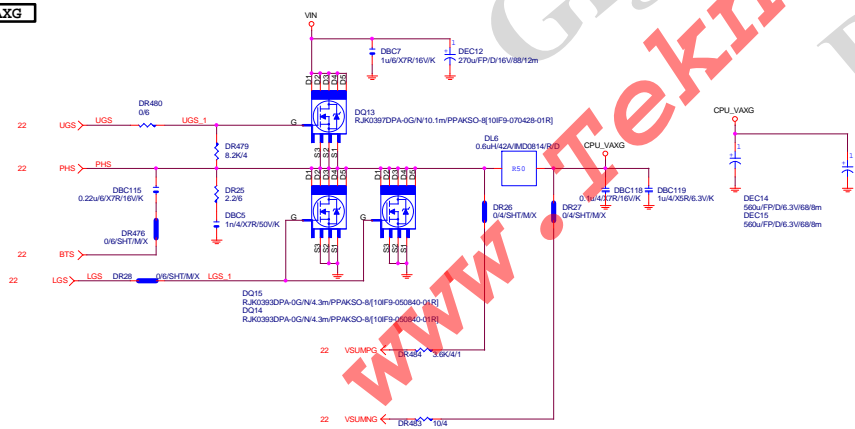
[3]



[2]

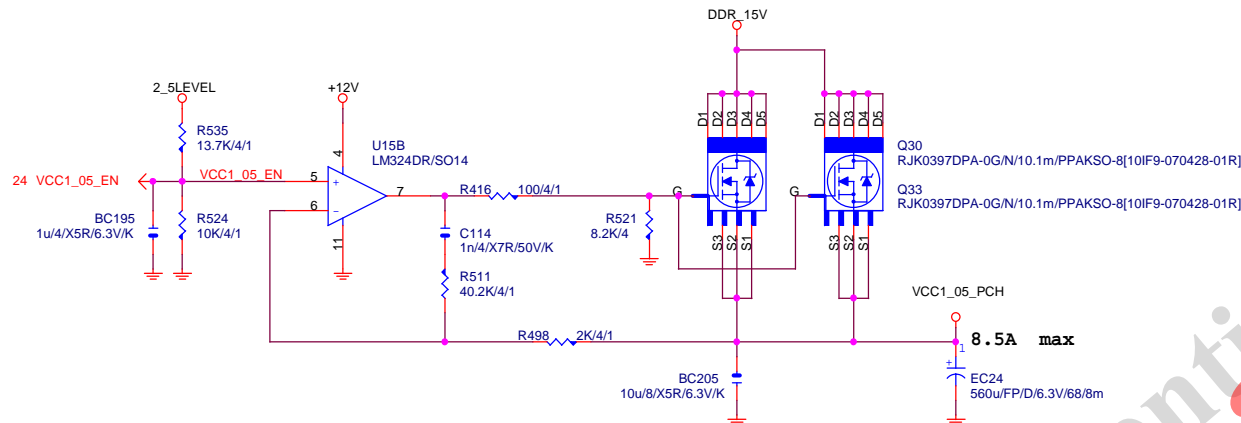


VAXG



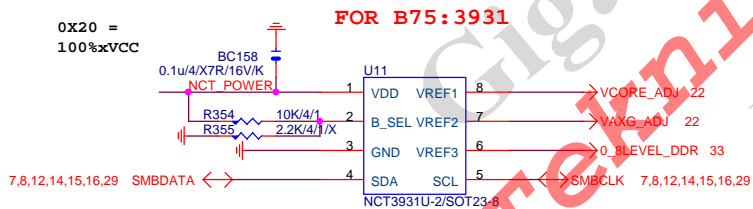
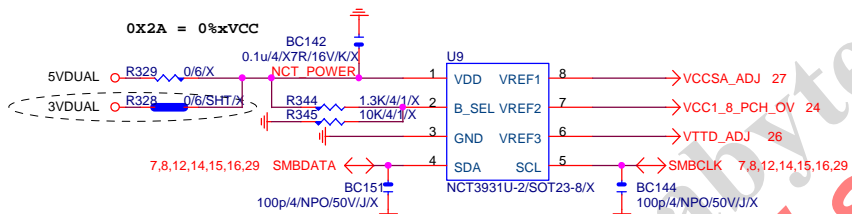


# VCC1\_05\_PCH



## Voltage console

ADDRESS	0X2A	0X20	0X22	0X26
R1 (K)	OPEN	10	1.3	3
R2 (K)	10	OPEN	3.9	2.2
%VCC	0	100	75	42

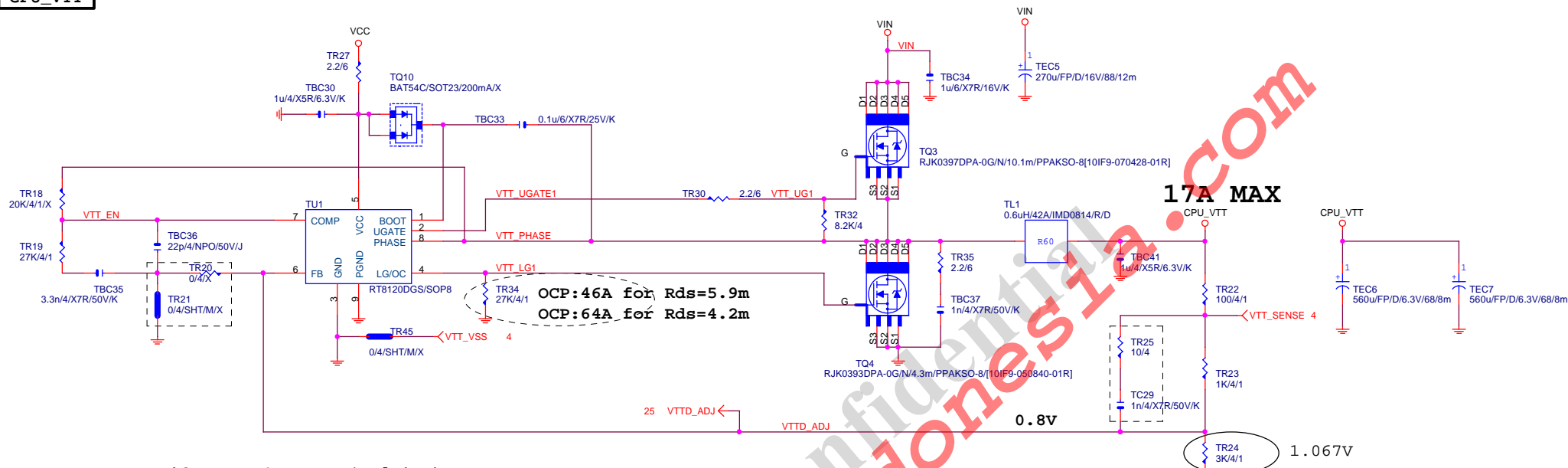


up6262	0X2A	0X20
VREF1	VCC1_05_PCH	VCORE
VREF2	VCC1_8_PCH	VCCSA
VREF3	CPU_VTT	DDR

Gigabyte Technology

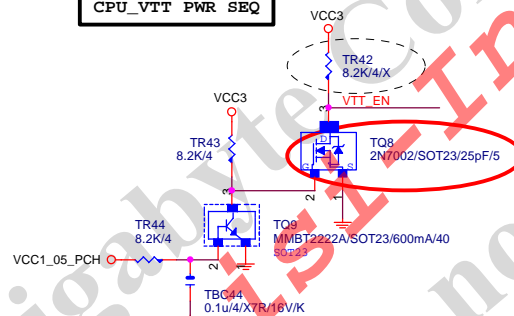
Title	PCH CORE / VOLTAGE CONSOLE		
Size B	Document Number	GA-B75-D3V	Rev 1.0
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# CPU\_VTT



$$OCP: 46A = \frac{R_{oset} \cdot I_{ocset}}{R_{ds(on)}} = \frac{27K \cdot 10\mu A}{5.9m}$$

## CPU\_VTT PWR SEQ



VTT_SEL	
HI	1.05V
LO	1.0V

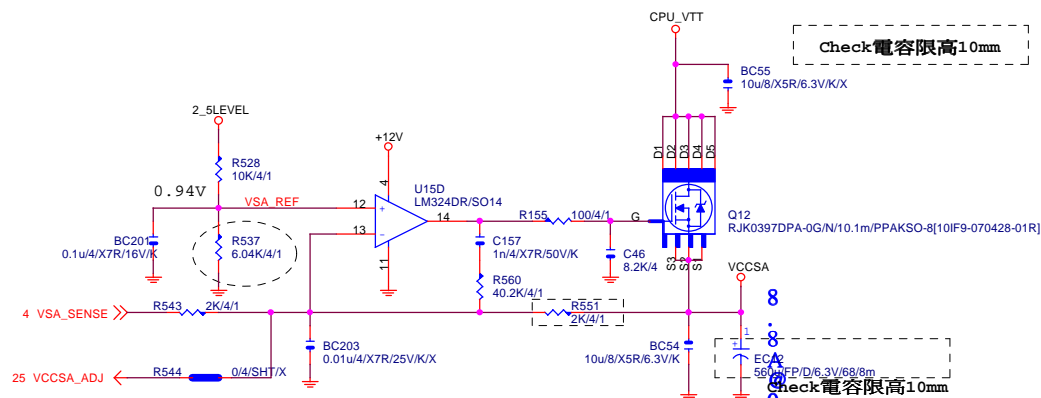
According intel  
CDI/IBP#476733, 固定1.05v

**GIGABYTE™**

Title	RT8120 CPU_VTT		
Size	Document Number	Rev	
Custom	GA-B75-D3V	1.0	
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VCC\_SA



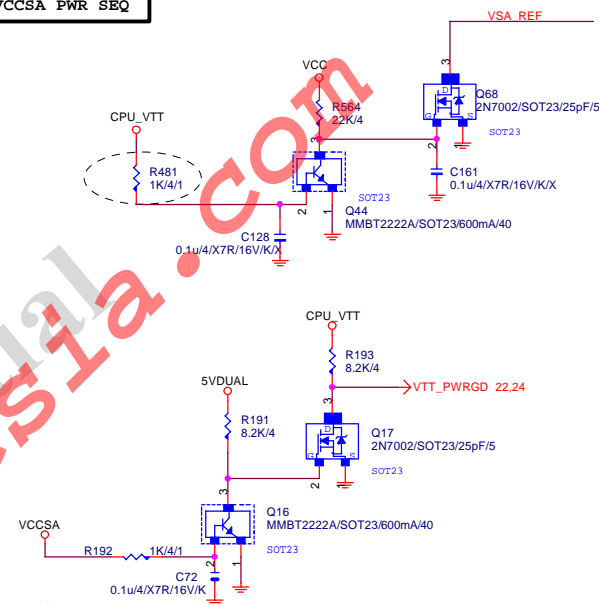
PDG 1.01

	VSA_SEL
HI	0.85V
LO	0.925V

According intel  
CDI/IBP#476733, 固定0.925V

8  
8  
5  
0  
9  
2  
5  
V

VCCSA PWR SEQ

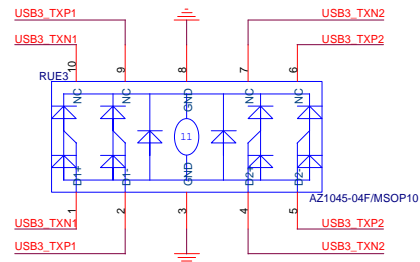
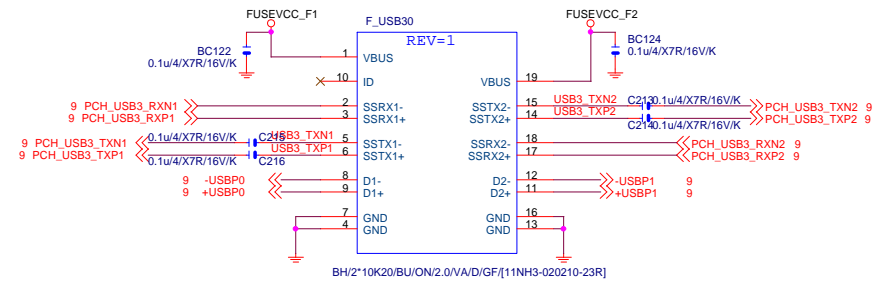


Gigabyte Technology

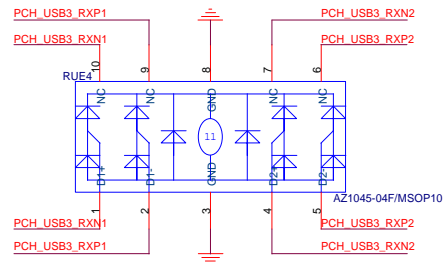
Title			
VCCSA POWER			
Size	Document Number	Rev	
Custom		GA-B75-D3V	
Date:	Monday, January 30, 2012	Sheet	27 of 34

1.0

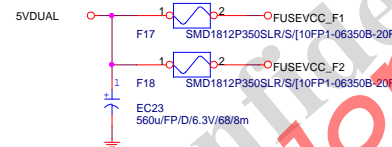
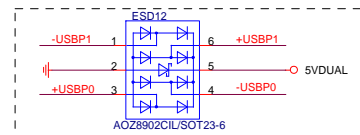
FRONT USB1



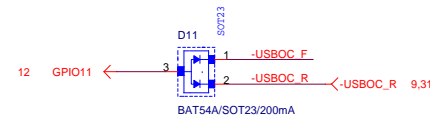
ESD Close to connector



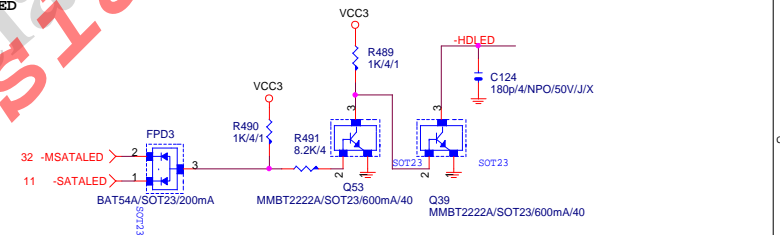
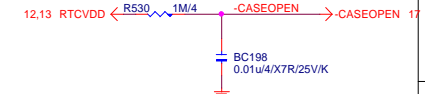
ESD Close to connector



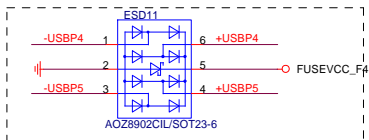
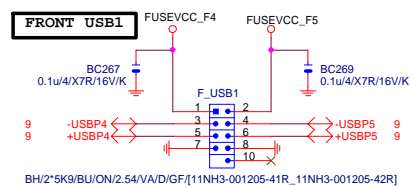
```
F_USB POWER PROTECT
```



## CASE OPEN

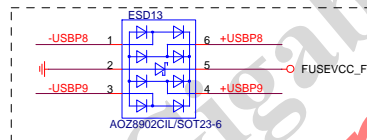
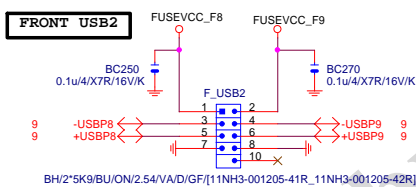


FRONT USB1

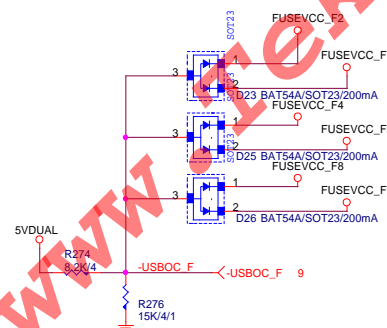
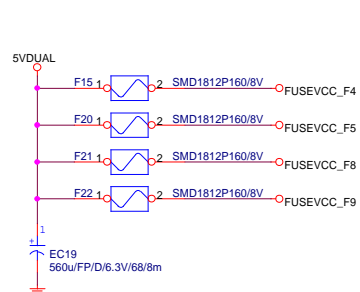


ESD Close to connector

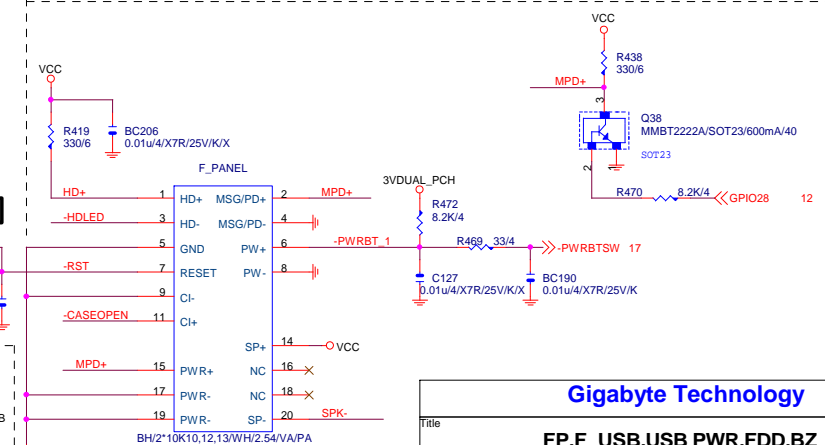
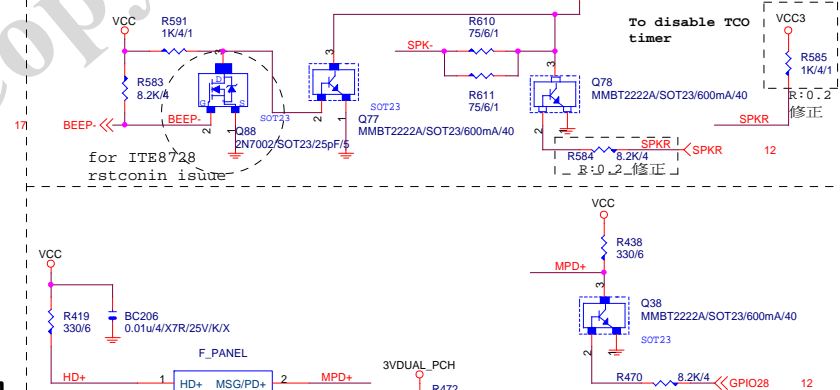
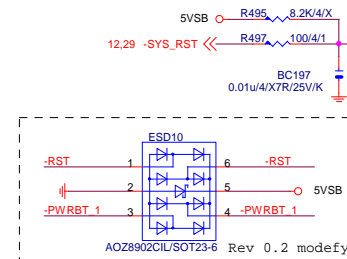
FRONT USB2



ESD Close to connector



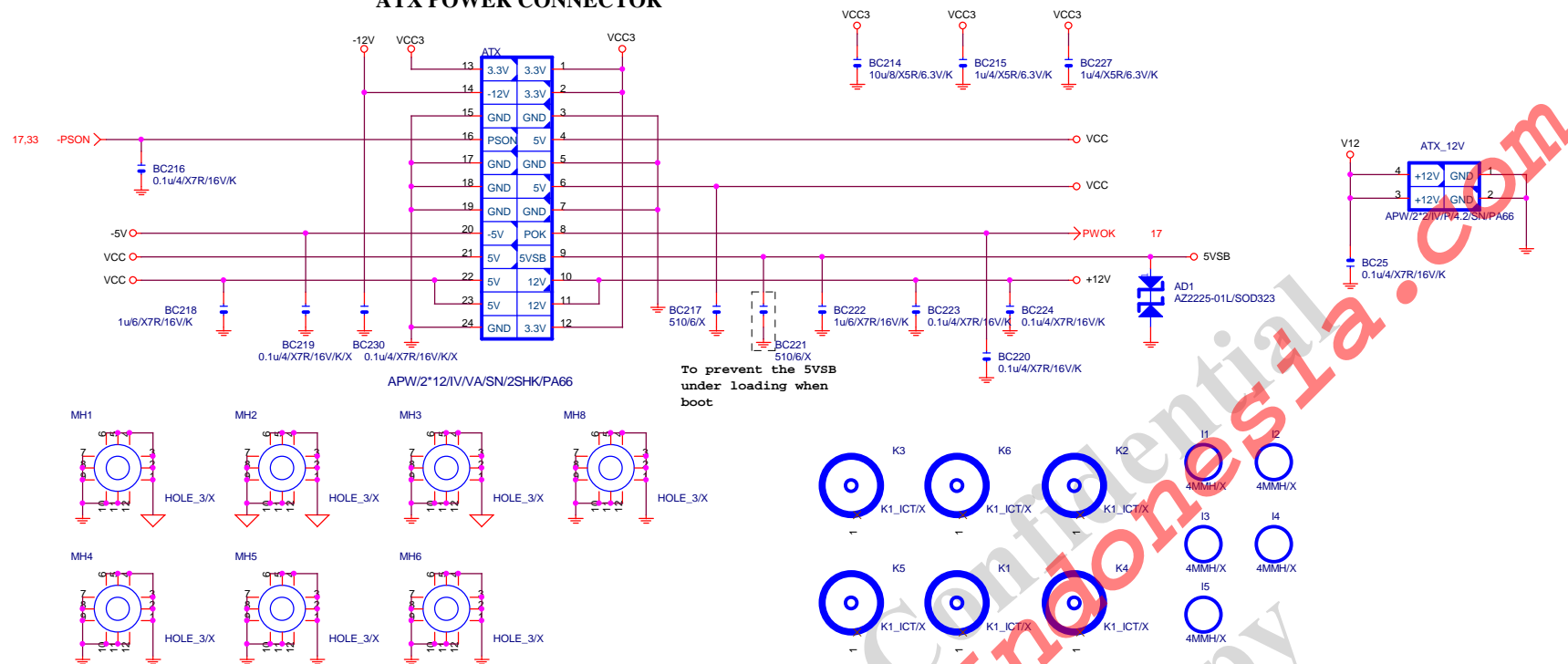
## INTEL FRONT PANEL



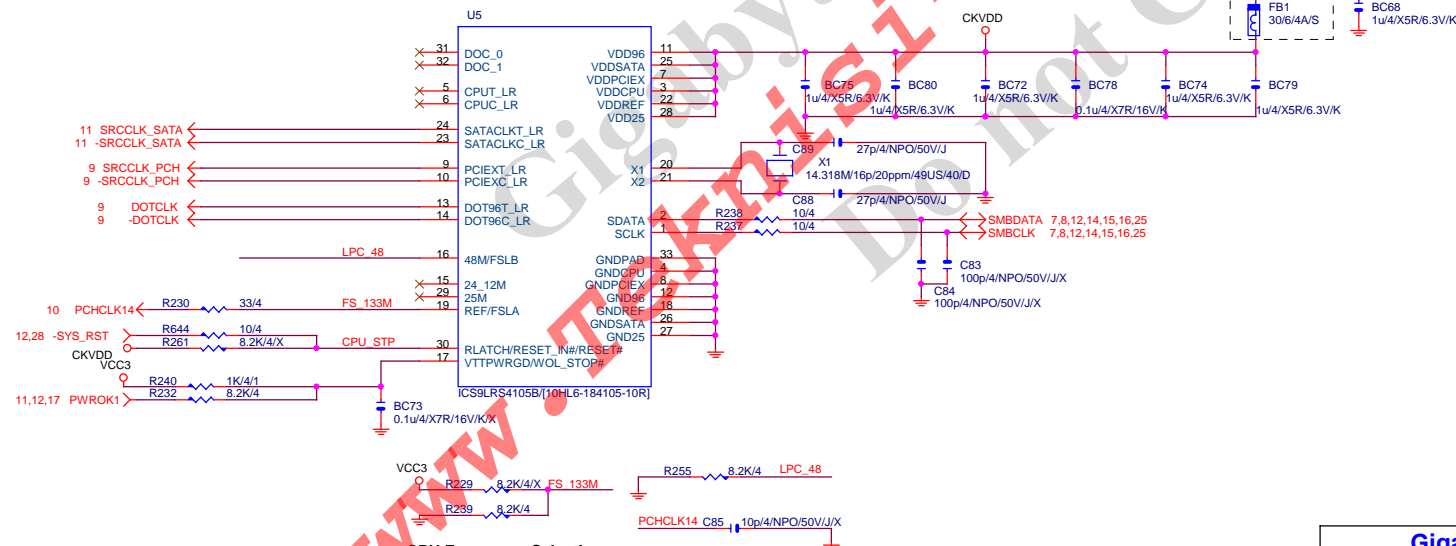
## Gigabyte Technology

Title			
FP,F_USB,USB PWR,FDD,BZ			
Size	Document Number		Rev
Custom	GA-B75-D3V		1.0
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# ATX POWER CONNECTOR



CLK GEN CK505



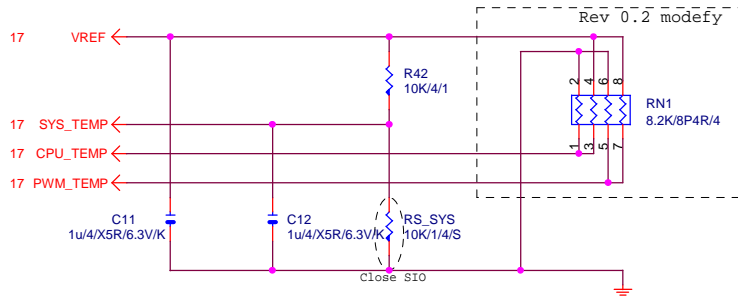
## CPU Frequency Selection

FS	CPU
0	100M <Default>
1	133M

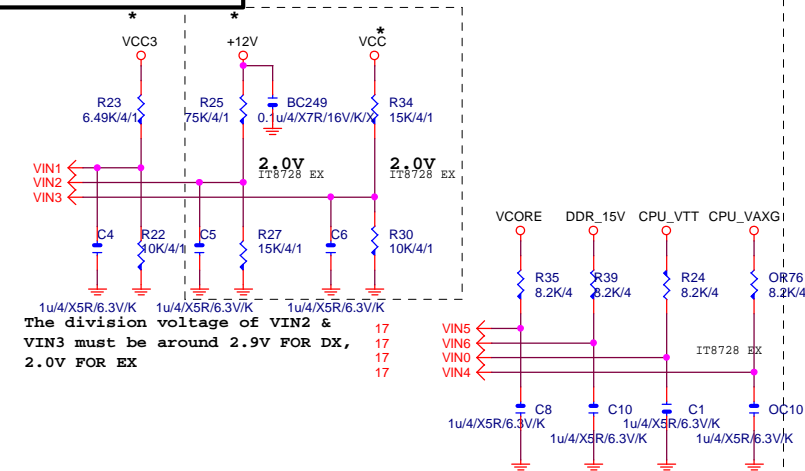
Gigabyte Technology

Title		
ATX POWER CONNECTOR		
Size	Document Number	Rev
Custom	GA-B75-D3V	1.0
Date: Monday, January 30, 2012 Sheet 29 of 34		

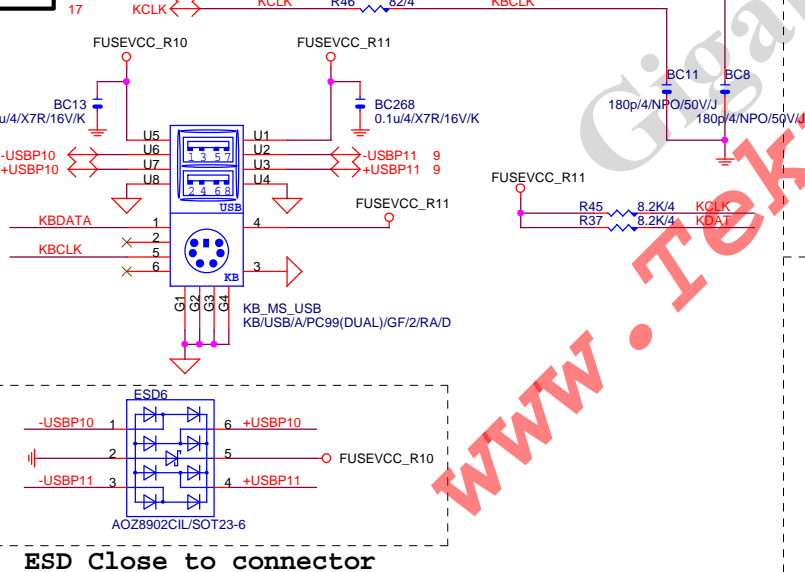
# TEMP H/W MONITOR



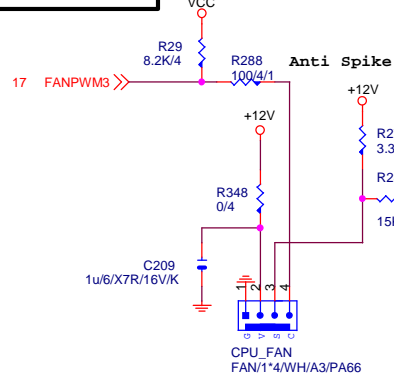
# VOLTAGE-- H/W MONITOR



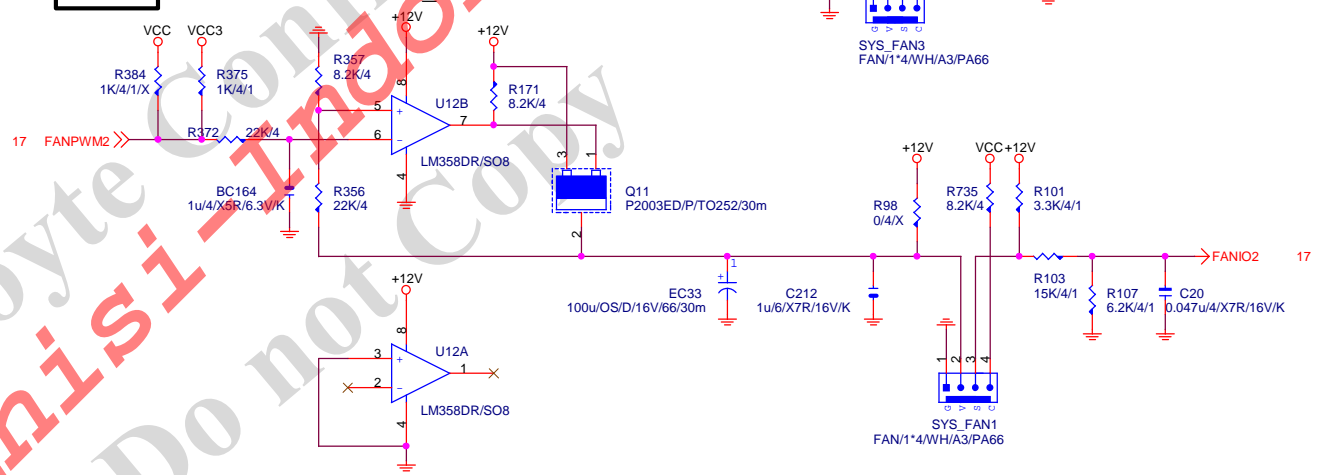
# KB/USB



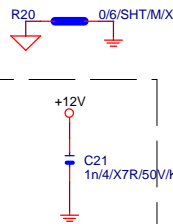
# CPU SMART FAN



# SYS FAN



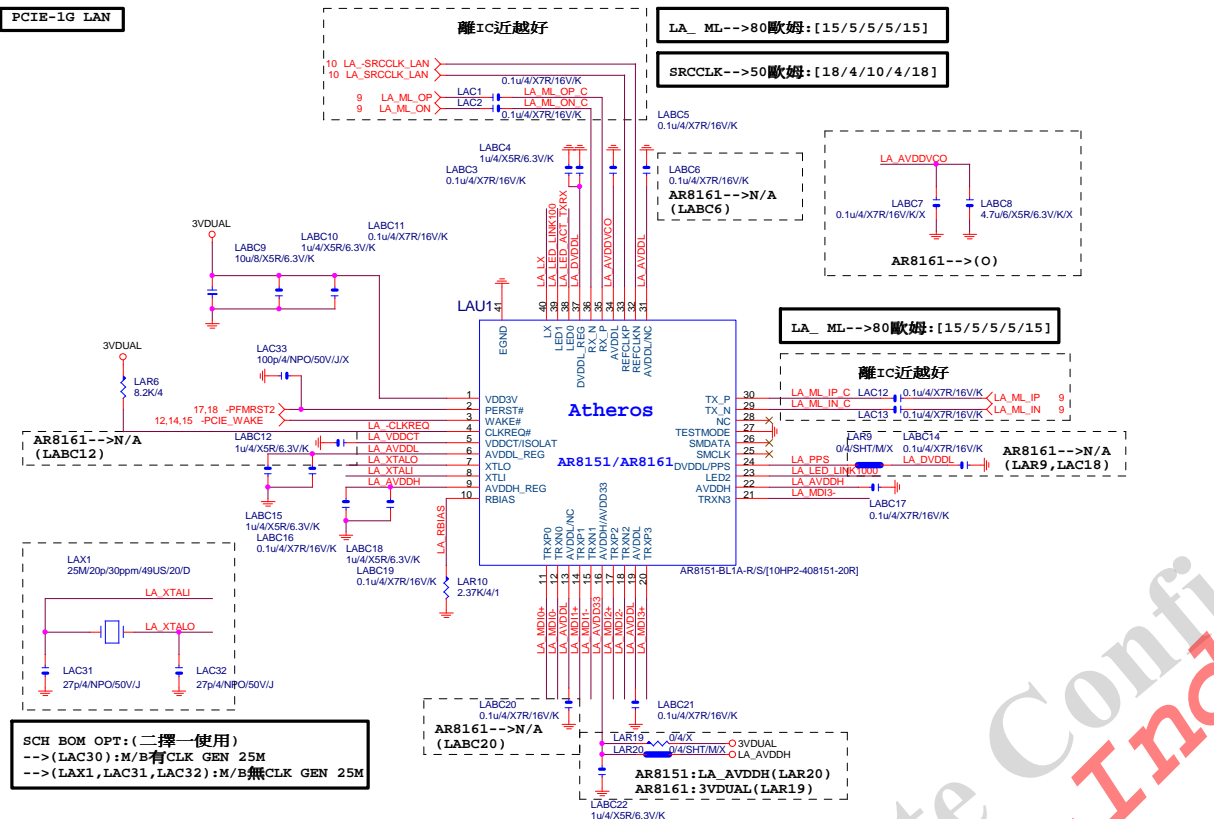
# FOR EMI ONLY



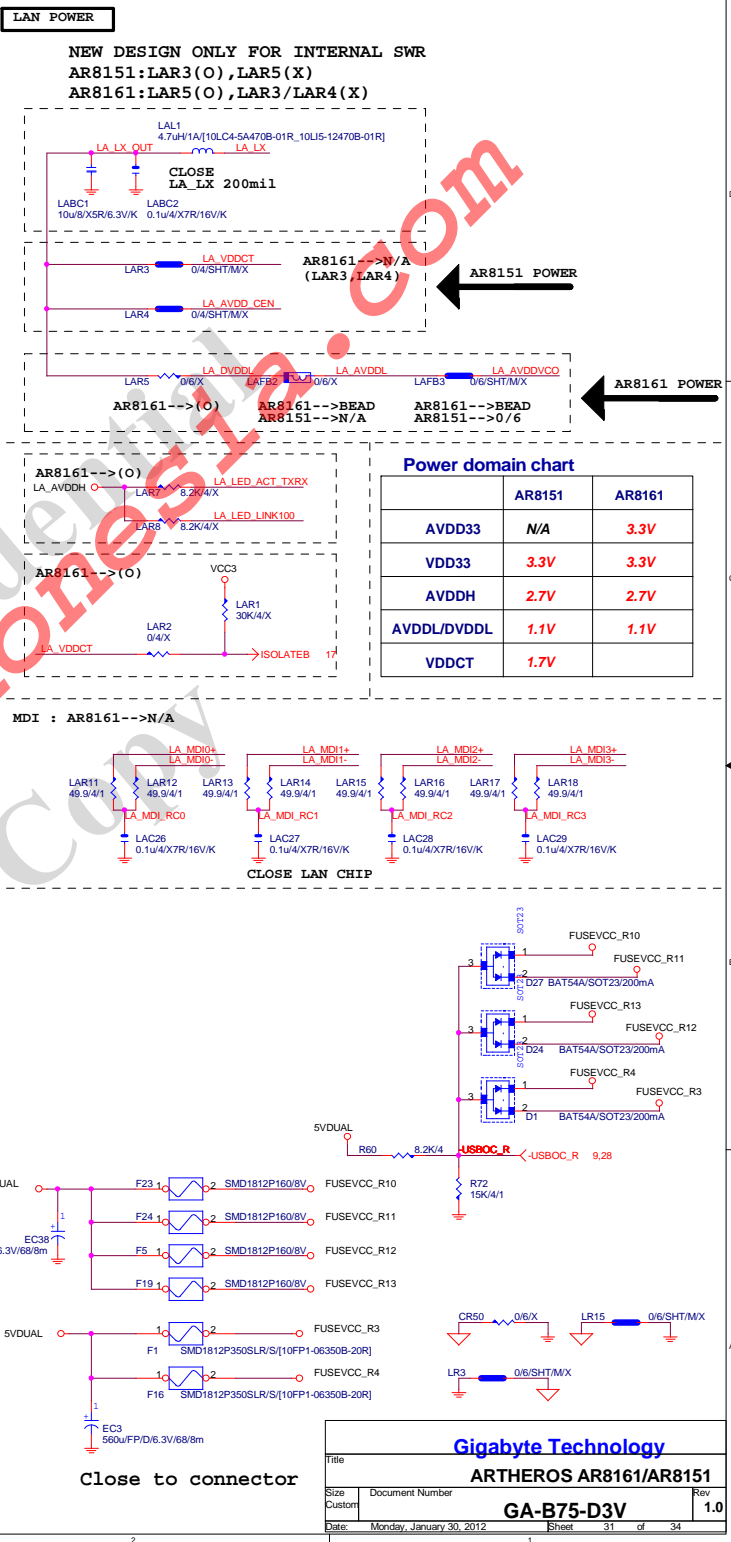
Gigabyte Technology

Title			HWM,KB/MS, FAN CTRL
Size	Document Number	Rev	
Custom	GA-B75-D3V	1.0	
Date:	Monday, January 30, 2012	Sheet	30 of 34

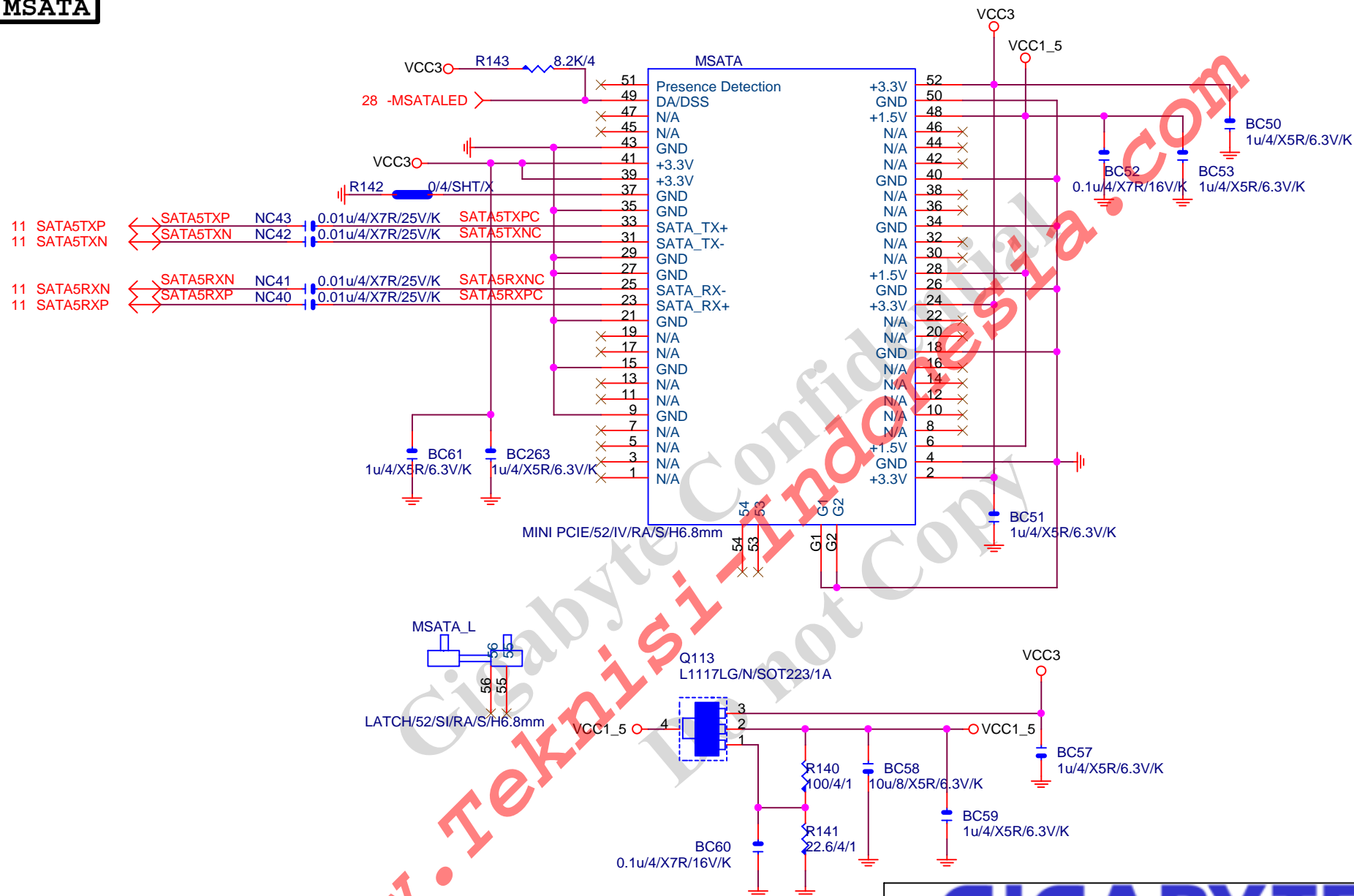
# PCIE-1G LAN



# LAN POWER



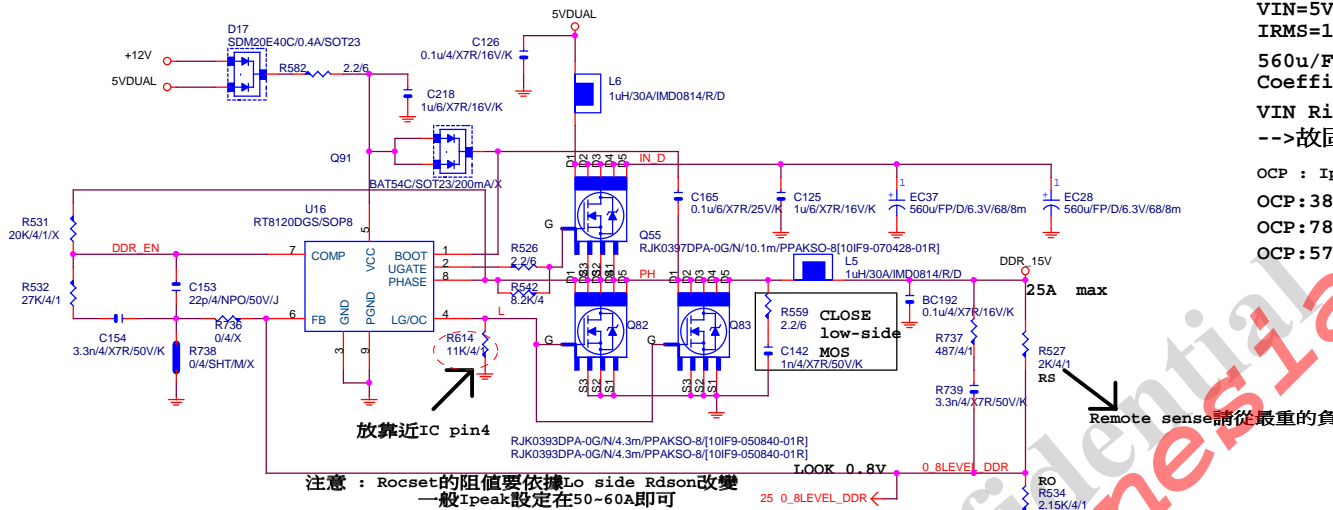
# MSATA



<b>GIGABYTE™</b>		
Title		
<b>MSATA</b>		
Size A	Document Number <b>GA-B75-D3V</b>	Rev <b>1.0</b>
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## DDR18V



VIN=5V, VOUT=1.5V, IOUT=25A, PHASE=1  
IRMS=11.45A  
560u/FP/D/6.3V/68/8m RIPPLE CURRENT=4.7A  
Coefficient=1.7(85°C), 1(105°C)  
VIN Ripple current=4.7x1.7=7.99A(85°C)  
-->故固態電容須2X7.99=15.98>11.45A

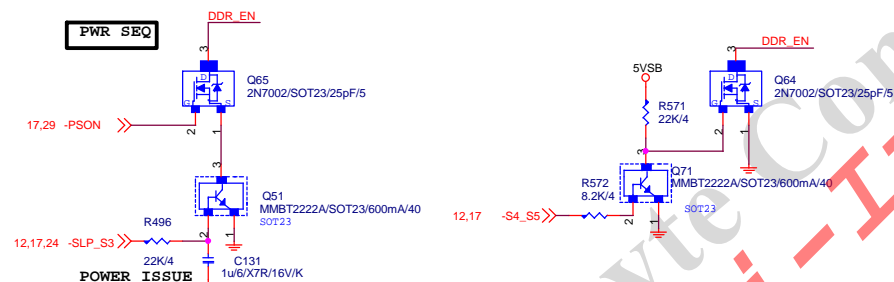
OCP :  $I_{peak} = (2 \times I_{ocset} \times R_{ocset}) / R_{ds(on)}$   
OCP: 38.31A for  $R_{ds} = 6.7m$  for vishay@4.5V  
OCP: 78.78A for  $R_{ds} = 3.3m$  for renesas@10V  
OCP:  $57A = R_{ocset} \times I_{ocset} / R_{ds(on)}$   
 $= 11k \times 10uA / [5 // 5]$

Remote sense請從最重的負載端點拉回

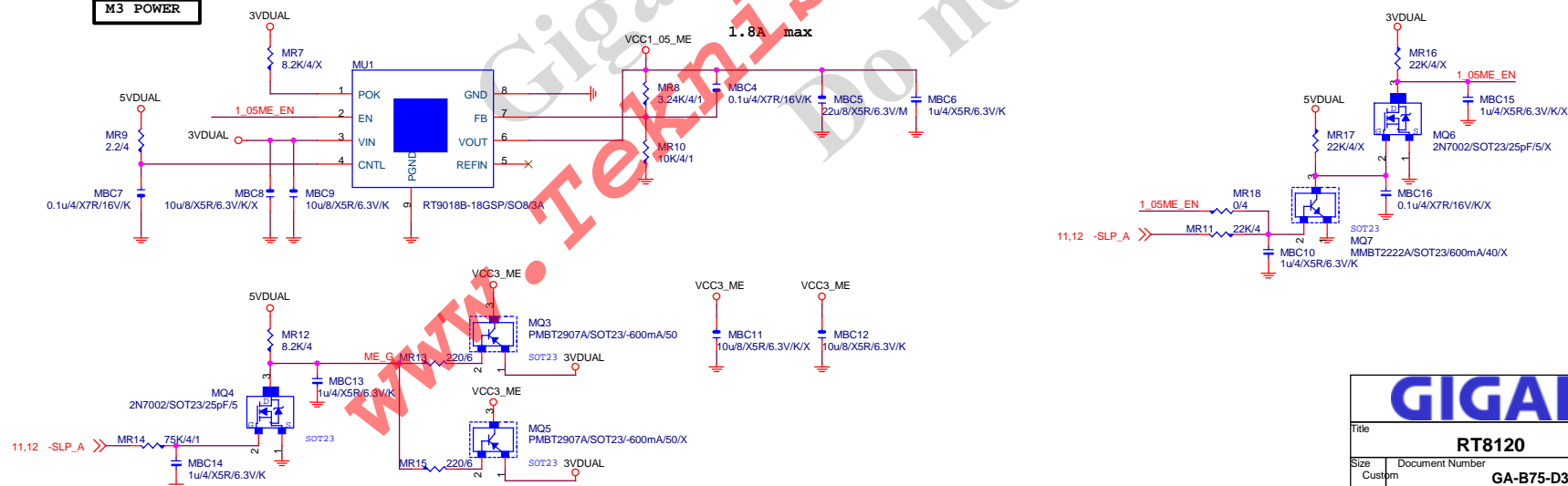
**注意**：Rocset的阻值要依據Lo side Rdson改變  
一般Ipeak設定在50~60A即可

$$0.8 * [(R_S + R_O) / R_O] = V_{out} = 1.54V$$

PWR SEQ



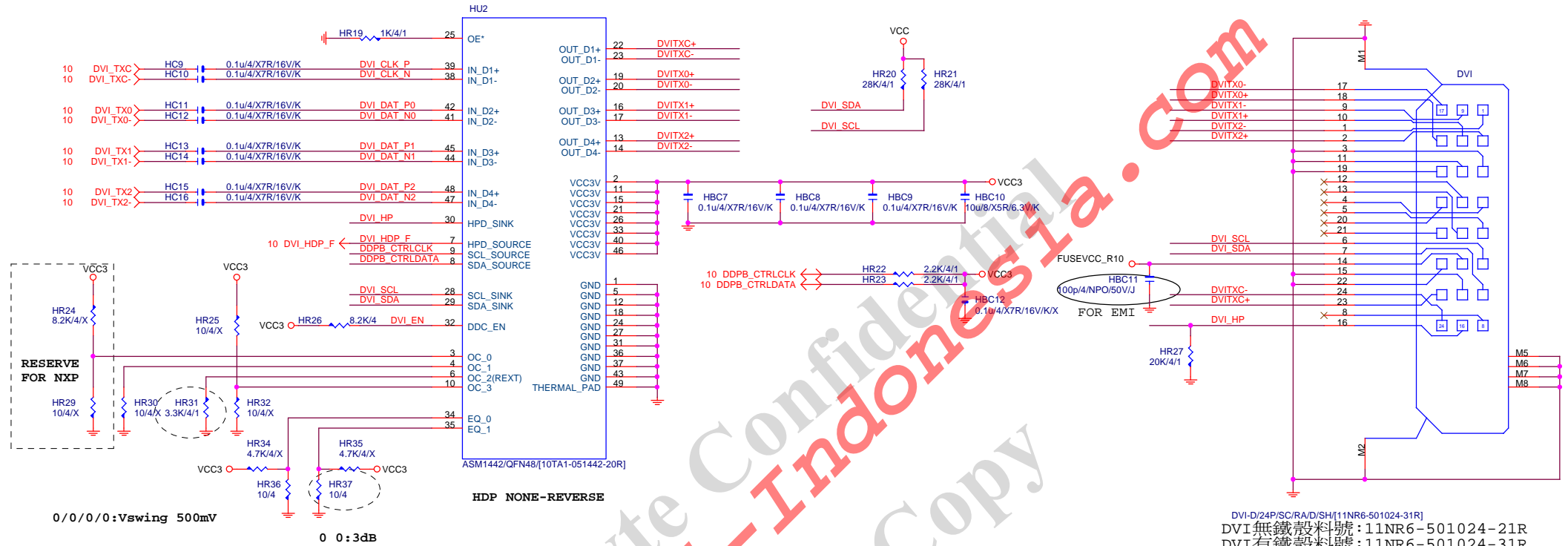
## M3 POWER



# GIGABYTE

Title			
RT8120			
Size	Document Number	Rev	
Custom	GA-B75-D3V	1.0	
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# DVI LEVEL SHIFT



Gigabyte Technology

Title			DVI
Size			Document Number
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Rev			1.0